JC07 Rec'd PCT/PTO 27 APR 2001

FORM PTO-1390 (Modified) (REV 11-98) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE TRANSMITTAL LETTER TO THE UNITED STATES DESIGNATED/ELECTED OFFICE (DO/EO/US) CONCERNING A FILING UNDER 35 U.S.C. 371		ATTORNEY'S DOCKET NUMBER 112740-190
		U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 09/830623
INTERNATIONAL APPLICATION NO. **PCT/DE99/03365**	INTERNATIONAL FILING DATE **October 27, 1999**	PRIORITY DATE CLAIMED **October 27, 1998**

TITLE OF INVENTION
RAKE RECEIVER FOR TELECOMMUNICATION SYSTEMS

APPLICANT(S) FOR DO/EO/US
Dr. Reinhold Braam et al.

Applicant herewith submits to the United States Designated/Elected Office (DO/EO/US) the following items and other information:

1. ☒ This is a **FIRST** submission of items concerning a filing under 35 U.S.C. 371.
2. ☐ This is a **SECOND** or **SUBSEQUENT** submission of items concerning a filing under 35 U.S.C. 371.
3. ☒ This is an express request to begin national examination procedures (35 U.S.C. 371(f)) at any time rather than delay examination until the expiration of the applicable time limit set in 35 U.S.C. 371(b) and PCT Articles 22 and 39(1).
4. ☒ A proper Demand for International Preliminary Examination was made by the 19th month from the earliest claimed priority date.
5. ☒ A copy of the International Application as filed (35 U.S.C. 371 (c) (2))
   a. ☒ is transmitted herewith (required only if not transmitted by the International Bureau).
   b. ☐ has been transmitted by the International Bureau.
   c. ☐ is not required, as the application was filed in the United States Receiving Office (RO/US).
6. ☒ A translation of the International Application into English (35 U.S.C. 371(c)(2)).
7. ☒ A copy of the International Search Report (PCT/ISA/210).
8. ☒ Amendments to the claims of the International Application under PCT Article 19 (35 U.S.C. 371 (c)(3))
   a. ☐ are transmitted herewith (required only if not transmitted by the International Bureau).
   b. ☐ have been transmitted by the International Bureau.
   c. ☐ have not been made; however, the time limit for making such amendments has NOT expired.
   d. ☒ have not been made and will not be made.
9. ☐ A translation of the amendments to the claims under PCT Article 19 (35 U.S.C. 371(c)(3)).
10. ☒ An oath or declaration of the inventor(s) (35 U.S.C. 371 (c)(4)).
11. ☒ A copy of the International Preliminary Examination Report (PCT/IPEA/409).
12. ☐ A translation of the annexes to the International Preliminary Examination Report under PCT Article 36 (35 U.S.C. 371 (c)(5)).

Items 13 to 20 below concern document(s) or information included:

13. ☐ An Information Disclosure Statement under 37 CFR 1.97 and 1.98.
14. ☐ An assignment document for recording. A separate cover sheet in compliance with 37 CFR 3.28 and 3.31 is included.
15. ☒ A **FIRST** preliminary amendment.
16. ☐ A **SECOND** or **SUBSEQUENT** preliminary amendment.
17. ☐ A substitute specification.
18. ☐ A change of power of attorney and/or address letter.
19. ☒ Certificate of Mailing by Express Mail
20. ☒ Other items or information:

Return Receipt Postcard

JC18 Rec'd PCT/PTO 2 7 APR 2001

U.S. APPLICATION NO. (IF KNOWN, SEE 37 CFR 09/830623	INTERNATIONAL APPLICATION NO. PCT/DE99/03365	ATTORNEY'S DOCKET NUMBER 112740-190

21. The following fees are submitted:.

BASIC NATIONAL FEE (37 CFR 1.492 (a) (1) - (5)) :

	CALCULATIONS	PTO USE ONLY
☐ Neither international preliminary examination fee (37 CFR 1.482) nor international search fee (37 CFR 1.445(a)(2) paid to USPTO and International Search Report not prepared by the EPO or JPO **$1,000.00**		
☒ International preliminary examination fee (37 CFR 1.482) not paid to USPTO but Internation Search Report prepared by the EPO or JPO **$860.00**		
☐ International preliminary examination fee (37 CFR 1.482) not paid to USPTO but international search fee (37 CFR 1.445(a)(2)) paid to USPTO **$710.00**		
☐ International preliminary examination fee paid to USPTO (37 CFR 1.482) but all claims did not satisfy provisions of PCT Article 33(1)-(4) **$690.00**		
☐ International preliminary examination fee paid to USPTO (37 CFR 1.482) and all claims satisfied provisions of PCT Article 33(1)-(4) **$100.00**		
ENTER APPROPRIATE BASIC FEE AMOUNT =	**$860.00**	
Surcharge of **$130.00** for furnishing the oath or declaration later than ☐ 20 ☐ 30 months from the earliest claimed priority date (37 CFR 1.492 (e)).	**$0.00**	

CLAIMS	NUMBER FILED	NUMBER EXTRA	RATE		
Total claims	9 - 20 =	0	x $18.00	**$0.00**	
Independent claims	1 - 3 =	0	x $80.00	**$0.00**	
Multiple Dependent Claims **(check if applicable).**			☐	**$0.00**	
	TOTAL OF ABOVE CALCULATIONS =			**$860.00**	

Reduction of 1/2 for filing by small entity, if applicable. Verified Small Entity Statement must also be filed (Note 37 CFR 1.9, 1.27, 1.28) **(check if applicable).**	☐	**$0.00**	
SUBTOTAL =		**$860.00**	
Processing fee of **$130.00** for furnishing the English translation later than ☐ 20 ☐ 30 months from the earliest claimed priority date (37 CFR 1.492 (f)). +		**$0.00**	
TOTAL NATIONAL FEE =		**$860.00**	
Fee for recording the enclosed assignment (37 CFR 1.21(h)). The assignment must be accompanied by an appropriate cover sheet (37 CFR 3.28, 3.31) **(check if applicable).**	☐	**$0.00**	
TOTAL FEES ENCLOSED =		**$860.00**	
		Amount to be: refunded	$
		charged	$

☒ A check in the amount of **$860.00** to cover the above fees is enclosed.

☐ Please charge my Deposit Account No. in the amount of to cover the above fees. A duplicate copy of this sheet is enclosed.

☒ The Commissioner is hereby authorized to charge any fees which may be required, or credit any overpayment to Deposit Account No. **02-1818** A duplicate copy of this sheet is enclosed.

NOTE: Where an appropriate time limit under 37 CFR 1.494 or 1.495 has not been met, a petition to revive (37 CFR 1.137(a) or (b)) must be filed and granted to restore the application to pending status.

SEND ALL CORRESPONDENCE TO:

William E. Vaughan, Esq.
Bell, Boyd & Lloyd LLC
P.O. Box 1135
Chicago, Illinois 60690-1135

SIGNATURE

William E. Vaughan
NAME

39,056
REGISTRATION NUMBER

April 27, 2001
DATE

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IN THE UNITED STATES ELECTED/DESIGNATED OFFICE
OF THE UNITED STATES PATENT AND TRADEMARK OFFICE
UNDER THE PATENT COOPERATION TREATY-CHAPTER II

PRELIMINARY AMENDMENT

APPLICANTS:	Dr. Reinhold Braam et al.	DOCKET NO: 112740-190
SERIAL NO:		GROUP ART UNIT:
		EXAMINER:
INTERNATIONAL APPLICATION NO:		PCT/DE99/03365
INTERNATIONAL FILING DATE:		27 October 1999
INVENTION:	RAKE RECEIVER FOR TELECOMMUNICATION SYSTEMS	

Assistant Commissioner for Patents,
Washington, D.C. 20231

Sir:

Please amend the above-identified International Application before entry into the National stage before the U.S. Patent and Trademark Office under 35 U.S.C. §371 as follows:

In the Specification:

Please replace the Specification of the present application, including the Abstract, with the following Substitute Specification:

SPECIFICATION

TITLE

RAKE RECEIVER FOR TELECOMMUNICATION SYSTEMS

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates, generally, to a rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, and, more particularly, to such a rake receiver

wherein a pipeline architecture having a number of pipeline stages is employed such that individual signal processing steps are processed as on a pipeline.

Description of the Prior Art

Telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers are special communication systems with an information transmission link between a message source and a message sink in which, for example, base stations and mobile parts are used as transceivers for message processing and transmission and in which:

1) the message processing and message transmission can take place in a preferred direction of transmission (simplex mode) or in both directions of transmission (duplex mode);

2) the message processing is preferably digital; and

3) the message transmission via the long-distance link takes place wirelessly on the basis of various message transmission methods FDMA (Frequency Division Multiple Access), TDMA (Time Division Multiple Access) and/or CDMA (Code Division Multiple Access) - e.g., according to radio standards such as

DECT [Digital Enhanced (previously European) Cordless Telecommunication; compare *Nachrichtentechnik Elektronik 42 (1992) Jan/Feb No. 1, Berlin, DE; U. Pilger "Struktur des DECT-Standards" [Structure of the DECT standard], pages 23 to 29* in conjunction with ETSI publication *ETS 300175-1 ... 9, October 1992* and DECT publication of the *DECT Forum, February 1997, pages 1 to 16*],

GSM [Group Spéciale Mobile or Global System for Mobile Communication; compare *Informatik Spektrum 14 (1991) June, No. 3, Berlin, DE; A. Mann: "Der GSM-Standard - Grundlage für digitale europäische Mobilfunknetze" [The GSM standard - The basis for digital European mobile radio networks], pages 137 to 152* in conjunction with the publication *telekom praxis 4/1993, P. Smolka "GSM-Funkschnittstelle*

- Elemente und Funktionen" [GSM radio interface - elements and functions], pages 17 to 24], UMTS [Universal Mobile Telecommunication System; compare (1): *Nachrichtentechnik Elektronik, Berlin 45, 1995 vol. 1, pages 10 to 14 and vol. 2, pages 24 to 27; P. Jung, B. Steiner: "Konzept eines CDMA-Mobilfunksystems mit gemeinsamer Detektion für die dritte Mobilfunkgeneration" [Concept of a CDMA mobile radio system with joint detection for the third mobile radio generation];* (2): *Nachrichtentechnik Elektronik, Berlin 41, 1991, vol. 6, pages 223 to 227 and page 234; P.W. Baier, P. Jung, A. Klein: "CDMA - ein günstiges Vielfachzugriffsverfahren für frequenzselektive und zeitvariante Mobilfunkkanäle" [CDMA - an advantageous multiple access method for frequency-selective and time-variant mobile radio channels];* (3): *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, vol. E79-A, No. 12, December 1996, pages 1 930 to 1 937; P.W. Baier, P. Jung: "CDMA Myths and Realities Revisited";* (4): *IEEE Personal Communications, February 1995, pages 38 to 47; A. Urie, M. Streeton, C. Mourot: "An Advanced TDMA Mobile Access System for UMTS";* (5): *telekom praxis, 5/1995, pages 9 to 14; P.W. Baier: "Spread-Spectrum-Technik und CDMA - eine ursprünglich militärische Technik erobert den zivilen Bereich" [Spread-spectrum technology and CDMA - a technology originally from the military domain conquers the civil domain];* (6): *IEEE Personal Communications, February 1995, pages 48 to 53; P.G. Andermo, L.M. Ewerbring: "A CDMA-Based Radio Access Design for UMTS";* (7): *ITG Fachberichte 124 (1993), Berlin, Offenbach: VDE Verlag ISBN 3-8007-1965-7, pages 67 to 75; Dr. T. Zimmermann, Siemens AG: "Anwendung von CDMA in der Mobilkommunikation" [Application of CDMA in mobile communication];* (8): *telcom report 16, (1993), vol. 1, pages 38 to 41; Dr. T. Ketseoglou, Siemens AG and*

Dr. T. Zimmermann, Siemens AG: "Effizienter Teilnehmerzugriff für die 3. Generation der Mobilkommunikation - Vielfachzugriffsverfahren CDMA macht Luftschnittstelle flexibler" [Efficient subscriber access for the 3rd-generation mobile communication - CDMA multiple access method makes the interface more flexible]; (9): *Funkschau 6/98: R. Sietmann "Ringen um die UMTS-Schnittstelle" [Tug-of-war for the UMTS interface], pages 76 to 81*] WACS or PACS, IS-54, IS-95, PHS, PDC etc. [compare IEEE Communications Magazine, January 1995, pages 50 to 57; D.D. Falconer et al.: "Time Division Multiple Access Methods for Wireless Personal Communications"].

"Message" is a higher-level term which stands both for the meaning (information) and for the physical representation (signal). In spite of identical meaning of a message, that is to say identical information, different signal forms can occur. Thus, for example, a message relating to an object can be transmitted

(1) in the form of an image,

(2) as a spoken word, or

(3) as a written word,

(4) as an encrypted word or image.

The type of transmission according to (1) ... (3) is normally characterized by continuous (analog) signals whereas it is usually discontinuous signals (e.g. pulses, digital signals) which are produced with the type of transmission according to (4).

In the UMTS scenario (3rd-generation mobile radio or, respectively, IMT 2000), there are two part-scenarios, for example according to the printed document *Funkschau 6/98: R. Sietmann "Ringen um die UMTS-Schnittstelle" [Tug-of-war for the UMTS interface], pages 76 to 81*. In a first part-scenario, the licensed coordinated mobile radio will be based on a WCDMA (Wideband Code Division Multiple Access) technology and operated in FDD (Frequency Division Duplex) mode as in GSM, whereas, in a second part-scenario, the unlicensed

uncoordinated mobile radio will be based on a TD-CDMA (Time Division Code Division Multiple Access) technology and operated in TDD (Time Division Duplex) mode as in DECT.

For the WCDMA/FDD operation of the universal mobile telecommunication system, the air interface of the telecommunication system in each case contains a number of physical channels in the uplink and downlink direction of telecommunication in accordance with the printed document *ETSI STC SMG2 UMTS-L1, Tdoc SMG2 UMTS-L1 163/98: "UTRA Physical Layer Description FDD Parts" vers. 0.3, 1998-05-29*, of which channels a first physical channel, the so-called Dedicated Physical Control Channel DPCCH, and a second physical channel, the so-called Dedicated Physical Data Channel DPDCH, are shown with respect to their frame structures in FIGURES 1 and 2.

In the downlink (radio link from the base station to the mobile station) of the WCDMA/FDD system by ETSI and ARIB, respectively, the Dedicated Physical Control CHannel (DPCCH) and the Dedicated Physical Data CHannel (DPDCH) are time-division multiplexed whereas in the uplink, an I/Q multiplex is done in which the DPDCH is transmitted in the I channel and the DPCCH is transmitted in the Q channel.

The DPCCH contains N_{pilot} pilot bits for channel estimation, N_{TPC} bits for fast power control and N_{TFI} format bits which indicate the bit rate, the type of service, the type of error protecting coding, etc. (TFI = Traffic Format Indicator).

FIGURE 3 shows, on the basis of a GSM radio scenario which includes, for example, two radio cells and Base Transceiver Stations arranged therein, a first base transceiver station BTS1 (transceiver) omnidirectionally illuminating a first radio cell FZ1 and a second base transceiver station BTS2 (transceiver) omni-directionally illuminating a second radio cell FZ2, an FDMA/TDMA/CDMA radio scenario in which the base transceiver stations BTS1, BTS2 are connected or can be connected to a number of mobile stations MS1 ... MS5 (transceiver) located in the radio cells FZ1, FZ2 by wireless unidirectional or bi-directional-

uplink UL and/or downlink DL - telecommunication on corresponding transmission channels TRC via an air interface designed for the FDMA/TDMA/CDMA radio scenario. The base transceiver stations BTS1, BTS2 are connected in at familiar manner (compare GSM telecommunication system) to a base station controller BSC which handles the frequency administration and switching functions in controlling the base transceiver stations. The base station controller BSC, in turn, is connected via a Mobile Switching Center MSC to the higher-level telecommunication network; e.g., the PSTN (Public Switched Telecommunication Network). The mobile switching center MSC is the administrative center for the telecommunication system shown. It handles the complete call administration and, with attached registers (not shown), the authentication of the telecommunication subscribers and the location monitoring in the network.

FIGURE 4 shows the basic configuration of the base transceiver station BTS1, BTS2 constructed as transceiver and FIGURE 5 shows the basic configuration of the mobile station MS1 ... MS5, also constructed as transceiver. The base transceiver station BTS1, BTS2 handles the transmitting and receiving of radio messages from and to the mobile station MS1 ... MS5 and the mobile station MTS1 ... MTS5 handles the transmitting and receiving of radio messages from and to the base transceiver station BTS1, BTS2. For this purpose, the base station has a transmitting antenna SAN and a receiving antenna EAN and the mobile station MS1 ... MS5 has a common antenna ANT for transmitting and receiving which is controllable by an antenna switch AU. In the uplink (receiving path), the base transceiver station BTS1, BTS2 receives via the receiving antenna EAN, for example, at least one radio message FN with an FDMA/TDMA/CDMA component from at least one of the mobile stations MS1 ... MS5 and the mobile station MS1 ... MS5 receives in the downlink (receiving path) via the common antenna ANT, for example, at least one radio message FN with an FDMA/TDMA/CDMA component from at least one base transceiver station

BTS1, BTS2. The radio message FN consists of a broadband spread-spectrum carrier signal modulated with an information item composed of data symbols.

In a radio receiver FEE, the received carrier signal is filtered and mixed down to an intermediate frequency which, in turn, is thereafter sampled and quantized. After analog/digital conversion, the signal, which has been distorted by multipath propagation on the radio path, is supplied to an equalizer EQL which largely equalizes (synchronizes) the distortions.

After that, a channel estimator KS attempts to estimate the transmission characteristics of the transmission channel TRC on which the radio message FN has been transmitted. The transmission characteristics of the channel are specified by the channel impulse response in the time domain. To be able to estimate the channel impulse response, a special supplementary information item in the form of a so-called midamble, which is designed as training information sequence, is assigned or allocated to the radio message FN at the transmitting end (by the mobile station MS1 ... MS5 or, respectively, the base transceiver station BTS1, BTS2, in the present case).

The individual mobile-station-specific signal components, which are contained in the common signal, are equalized and separated in a known manner in a subsequent data detector DD which is common to all received signals. After the equalization and separation, the data symbols hitherto present are converted into binary data in a symbol-to-data converter SDW. After that, the original bit stream is obtained from the intermediate frequency in a demodulator DMOD before the individual time slots are allocated to the correct logical channels and, thus, also to the different mobile stations in a demultiplexer DMUX.

The bit sequence obtained is decoded channel by channel in a channel codec KC. Depending on the channel, the bit information is allocated to the control and signaling timeslot or to a voice timeslot and, in the case of the base transceiver station (FIGURE 4), the control and signaling data and the voice data are jointly transferred to an interface SS responsible for the signaling and voice

coding/decoding (voice codec) for transmission to the base station controller BSC. In the case of the mobile station (FIGURE 5), the control and signaling data are transferred to a control and signaling unit STSE responsible for the complete signaling and control of the mobile station and the voice data are transferred to a voice codec SPC designed for voice input and output.

In the voice codec of the interface SS in the base transceiver station BTS1, BTS2, the voice data is in a predetermined data stream (e.g., 64-kbit/s stream in the direction of the network and 13 kbit/s stream in the direction from the network).

The complete control of the base transceiver station BTS1, BTS2 is performed in a control unit STE.

In the downlink (transmitting path), the base transceiver station BTS1, BTS2 sends via the transmitting antenna SAN, for example, at least one radio message FN with an FDMA/TDMA/CDMA component to at least one of the mobile stations MS1 ... MS5 and the mobile station MS1 ... MS5 sends in the uplink (transmitting path) via the common antenna ANT, for example, at least one radio message FN with an FDMA/TDMA/CDMA component to at least one base transceiver station BTS1, BTS2.

The transmitting path begins at the base transceiver station BTS1, BTS2 in FIGURE 4, by control and signaling data and voice data received by the base station controller BSC via the interface SS being assigned to a control and signaling timeslot or a voice timeslot in the channel codec KC and these timeslots being coded in a bit sequence channel by channel.

The transmitting path begins in the case of the mobile station MS1 ... MS5 in FIGURE 5 by voice data received from the voice codec SPC and control and signaling data received from the control and signaling unit STSE being assigned to a control and signaling timeslot or a voice timeslot in the channel codec KC and these timeslots being coded in a bit sequence channel by channel.

The bit sequence obtained in the base station BTS1, BTS2 and in the mobile station MS1 ... MS5 is, in each case, converted into data symbols in a data-to-symbol converter DSW. Following this, the data symbols are, in each case, spread with a subscriber-associated code in a spreader SPE. In the burst generator BG consisting of a burst assembler BZS and a multiplexer MUX, a training information sequence in the form of a midamble is then added to the spread data symbols in the burst assembler BZS for channel estimation and the burst information obtained in this manner is set to the correct timeslot in the multiplexer MUX. The burst obtained is then radio-frequency modulated, in each case, in a modulator MOD and digital/analog converted before the signal obtained in this manner is radiated at the transmitting antenna SAN or, respectively, the common antenna ANT via a radio transmitter FSE as radio message FN.

In CDMA-based systems, the problem of multiple reception, the so-called delay spread, in the presence of echoes can be solved in spite of the great bandwidth and the very small chip or bit times of these systems by combining the received signals with one another in order to increase the reliability of detection. Naturally, the channel characteristics must be known for this. To determine these, a pilot sequence common to all subscribers is used (compare FIGURES 1 and 2) which is additionally radiated independently and with increased transmitting power without modulation by a message sequence. Its reception provides the receiver with the information on how many paths are involved in the instantaneous situation of reception and what delay times are produced.

In a RAKE receiver, the signals coming in via the individual paths are acquired in separate receivers, the "fingers" of the RAKE receiver, detected and added together in an addition section weighted among each other after compensation for the delay times and the phase shifts of the echoes.

A RAKE receiver is used, in particular, for recovering digital data from a radio reception signal having a CDMA component. The signals superimposed via

multipath propagation and distorted by the channel are recovered and the symbol energies of the individual propagation paths are accumulated.

The theory for the RAKE receiver has been sufficiently well investigated and is known (compare J.G. Proakis: "Digital Communications"; McGraw-Hill, Inc.; 3rd edition, 1995; pp. 728 to 739 and K.D. Kammeyer: "Nachrichtenübertragung" [Information transmission]; B:G. Teubner Stuttgart, 1996; pp. 658 to 669).

An object of the present invention is to specify a rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, especially in third-generation mobile radio systems, which exhibits a smaller number of function blocks and/or logic gates compared with known rake receivers.

Accordingly, the present invention has a pipeline architecture, including a number of pipeline stages (pipeline structure), which is implemented such that the individual signal processing steps or computing steps are processed as in a pipeline. As a result, the hardware circuits used can be used in time-division multiplex.

In an embodiment, it is advantageous to use three pipeline stages. It is also advantageous to buffer the processing in the pipeline stages via two registers if no direct pipelining is possible in the three pipeline stages because of different processing speeds in the pipeline stages.

In a first pipeline stage, the data (e.g., chips or subchips in the case of oversampling) are read out of a memory (e.g., a dual-port RAM (DP-RAM)). To be able to superimpose the symbols of the individual signal paths in the correct phase (code combining), the corresponding path delays must be taken into consideration. The addresses are also calculated in the first stage. The delay time is added to the current address in the form of an offset. There are, for example, "L" offsets, "L" corresponding to the number of fingers in the RAKE receiver and

a different offset being needed in each clock period. Thus, the memory is accessed in every clock period.

Furthermore, the code generated by at least one code generator, the spreading code and/or the scrambling code required for descrambling, is multiplied by the current value from the dual-port RAM in the first pipeline stage. This operation is relatively simple since it only consists of sign operations and of two additional additions in the case of complex scrambling codes.

In addition, the soft handover is handled in the first pipeline stage. In the case of a soft handover, the RAKE receiver receives signals which have been sent with different scrambling and spreading codes from, for example, base transceiver stations. The maximum possible number of RAKE fingers must be shared out among the base transceiver stations in accordance with the quality of reception. For this reason, the code generators are switched in dependence on the RAKE fingers. The multiplexer performing the switching operates at a maximum of L* W MHz. To increase the number of base transceiver stations, further code generators can be added.

In the second pipeline stage, each value is multiplied by a weight. These weights are different for each finger and change with every clock period. In principle, they are repeated after "L" steps. In the case of an interpolation, the delta values are accumulated to form the weights.

In the last, third pipeline stage, the chip energies of the individual RAKE fingers are accumulated to form the symbol energy U_{symb}.

$u_{symb} = \sum_{i=1}^{SF} \sum_{j=1}^{L} u_{ij}$; where SF = spreading factor, L = number of RAKE fingers.

Additional features and advantages of the present invention are described in, and will be apparent from, the Detailed Description of the Preferred Embodiments and the Drawings.

DESCRIPTION OF THE DRAWINGS

Figures 1 and 2 show, for the WCDMA/FDD operation of the universal mobile telecommunication system, the Dedicated Physical Control Channel and the Dedicated Physical Data Channel of the air interface of a telecommunication system with respect to their frame structures;

Figure 3 shows, on the basis of the GSM radio scenario, first and second base transceiver stations;

Figure 4 shows the basic configuration of the base transceiver station constructed as a transceiver;

Figure 5 shows the basic configuration of the mobile station constructed as a transceiver; and

Figure 6 shows, in block diagram form, rake receivers having a pipeline architecture.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

1. Time-division multiplexing of the RAKE pipeline architecture

In the known architectures, each finger of the RAKE receiver is implemented individually, the chips are accumulated to form symbols and, at the end, the sum over all fingers is formed. In the case of "L" fingers, this leads to the following hardware requirement:

- $L + 1$ adder and
- $2 * L$ multipliers (complex multiplication)

If the signal processing chain for a RAKE finger is set up as a pipeline, a single pipelined RAKE finger can emulate a complete RAKE receiver in time-division multiplex. This is only limited by the number of fingers and maximum clock rate of the available technology. This reduces the complexity to

- 1 adder,

- 2 multipliers and
- b+2*m additional registers,

where “b” is the maximum number of base transceiver stations involved in the soft handover and “m” is the number of fingers to be corrected for the early-late tracking.

2. Code combing via dual-port RAM accesses

To be able to superimpose the symbols of the individual signal paths in the correct phase (code combining), the corresponding path delays must be taken into consideration. Various known approaches to a solution use shift registers and a relatively elaborate multiplexer logic for this purpose.

In the proposed approach to the solution, a simple dual-port RAM (DP-RAM) is used. Code combining is done by selectively using address offsets which correspond to the delay between the different propagation paths.

Instead of the dual-port RAM, SRAMs, SDRAMs or SSRAMs can also be used which emulate a DP-RAM.

3. Interpolation of the weights

To reduce the number of channel estimations for calculating the conjugate complex coefficients (weights) or, respectively, to achieve a smaller time deviation from the ideal value of these estimations, it is possible to determine the coefficients between two estimations via *interpolation*. This simplification in the channel estimation can be easily integrated into the pipeline architecture.

4. Early-late tracking of the RAKE fingers

The prerequisite for acceptable bit error rates is to position the RAKE fingers as accurately as possible. The position of the individual RAKE fingers is determined with the aid of an elaborate matched filter. The length of the channel, the required accuracy in the positioning of the fingers and the frequency of calculations performed determine the expenditure for the matched filter. Any more inaccurate, initial determination of the finger position performed at greater time intervals lead to a considerable reduction in the expenditure for the matched filter. To counteract the resultant degradation, the so-called early/late tracking is used. The early finger is positioned one half chip before, and the late finger one half chip after, the RAKE finger to be positioned (main finger). The energies of the early and late finger are calculated in the last stage of the RAKE receiver and only require little complexity. If the energies of the two fingers ≈ 0, i.e. they have approximately the same low energy, the enclosed finger, the main finger, has an almost optimum position. If the energies of the tracking fingers are not approximately equal or $\neq 0$, a repositioning takes place at W/n intervals, "W" being the chip frequency and "n" being the oversampling rate.

5. Soft handover

In the soft handover, the RAKE receiver receives signals sent with different scrambling/spreading codes from a number of base transceiver stations. The maximum number of RAKE fingers must be shared among the base transceiver stations in accordance with the quality of reception. This requires switching of the code generators which is dependent on the RAKE fingers. The multiplexer performing the switching operates with a maximum of L * W MHz, taking into consideration the early/late fingers.

During the soft handover, the base transceiver stations involved send the same user data to the mobile station. To control the transmitting power of the mobile station, the latter additionally receives an information item, the so-called TPC (transfer power control, compare FIGURES 1 and 2) bit which says whether

the transmitting power has to be increased or decreased. For this reason, the different base transceiver station-dependent TPC bits must be decoded. The concluding or last part of the processing pipeline accumulates for this purpose symbols representing TPC bits, separated in accordance with received base transceiver station.

6. Flexibility of the architecture with respect to word widths, clock rates and parallel processing

Depending on the field of application and the quality required (e.g., bit error rate) of the communication link (data, voice, etc.), a different number of RAKE fingers and word widths are required in the signal processing path. The proposed architecture allows simple adaptation. Greater word widths require lower clock rates of the individual processing units, the technology remaining the same. The processing power of the RAKE pipeline architecture can be increased by inserting parallel processing branches without greatly increasing circuit complexity. This provides for higher clock rates.

In the implementation of a RAKE receiver in hardware and/or software, however, savings can be achieved with respect to the number of function blocks used or their complexity by suitable mapping in software and hardware, and a greater flexibility in the parametrization; e.g., number of RAKE fingers.

In addition, the availability of fast technologies in the field of chip design (e.g., ASIC, FPGA) allows essential parts of the hardware to be used in time-division multiplex and, thus, to reduce the necessary number of logic gates.

An exemplary embodiment of the present invention is explained with reference to FIGURE 6.

FIGURE 6 shows, in block diagram form, RAKE receivers having a pipeline architecture, consisting of three pipeline stages, a first pipeline stage PLS1, a second pipeline stage PLS2 and a third pipeline stage PLS3 for L=8

fingers, soft handover with two base transceiver stations and early/late tracking. The pipeline structure shown is designed for one finger, but all fingers can be successively corrected. The clock rates specified relate to the RAKE receiver thus specified and are, therefore, a multiple of the chip frequency of 4 096 MChip. The word widths specified within the signal processing chain are derived from the boundary conditions for UMTS standardization (compare *SMG2 UMTSPhysical Layer Expert Group: "UTRA Physical Layer Description FDD Parts" vers. 0.4, 1998-06-25).*

In principle, the architecture described can be extended to a different chip frequency "W", to any number of fingers "L", to "b" possible base transceiver stations in the case of a soft handover and 2*L fingers for the early/late tracking. Similarly, the architecture is flexible with respect to the choice of word widths used in the signal processing path.

The received signal r(t) is written in a dual-port RAM (DP-RAM) DPR with a frequency of 4 096 * n MHz (where n is the oversampling rate). The addresses for storing input data (chips) in the dual-port RAM DPR are generated by a first address counter AZ1.

To read the received chips out of the dual-port RAM DPR, an address (8 * 4 096 MHz clock) is calculated from the addition of a free-running second address counter AZ2 and the offsets dependent on the RAKE finger. The offsets are located in offset registers. For the early/late finger tracking to be implemented, two of the offset registers can be used for positioning the early and late finger. To recover the symbols, the data read out is multiplied in a first multiplier MUL1 by a spreading code generated by at least one code generator (two code generators CG1, CG2 in FIGURE 6) and/or a scrambling code required for descrambling. In the case of simple codes, this is a sign operation whereas an additional addition is added in the case of complex codes.

MUX which perform. In the soft handover case, the RAKE receiver receives signals sent with different scrambling/spreading codes from, e.g., two base

transceiver stations, base transceiver station 1 and base transceiver station 2. The maximum possible number of RAKE fingers must be shared among the base transceiver stations in accordance with the quality of reception. The scrambling/spreading codes are selected in a code combining/soft handover circuit CCSHS. This is why the code generators CG1, CG2 need to be switched in dependence on the RAKE fingers. A multiplexer MUX which performs the switching operates with a maximum of 8 * 4 096 MHz in this example. In addition, the corresponding path delays are taken into consideration in this circuit CCSHS to be able to obtain a superimposition of the symbols of the individual signal paths in the correct phase (code combining).

The channel necessary for transmission distorts the signal. In the second pipeline stage PLS2, the channel estimator calculates the conjugate complex channel coefficients (weights) necessary for correcting the distortion from the received pilot sequence. The receiver, therefore, multiplies the recovered symbols of the individual RAKE fingers by their weights C_i^* in a second multiplier MUL2. These weights are stored in a ring memory.

To avoid frequent estimation of the channel because this is a computationally intensive process, and to achieve a smaller time deviation of the coefficients from the ideal value, the weights are interpolated between two estimations in interpolation part IPM. This results in continual adding together of delta values.

In the last pipeline stage, the third pipeline stage PLS3, the chip energies of the individual fingers and thus the levels belonging to a symbol are accumulated in an accumulator AK successively over the period of one symbol. Symbols which represent TPC (transfer power control) bits must be accumulated separated by received base transceiver station. After each symbol, the accumulator AK must be reset.

For the early/late tracking, two separate accumulator registers AKR must be additionally provided for each early/late finger.

For each timeslot, overflow detector ÜD registers a bit overflow produced and deletes it at the beginning of the new timeslot.

If an overflow occurs, an AGC control ACGR must be informed that the input gain must be decreased.

The estimated value of the symbol $\underline{U}_m$ is present at the output of the RAKE receiver.

The following expression represents the general calculation of the estimated value $\underline{U}_m$ of a received symbol:

$$\underline{\quad}_m = \int_0^T \underline{r}(t) * \sum_{n=1}^{L} \underline{c}_n(t) * \underline{q}(t - n / W)dt$$

where r(t) is the received signal, $\underline{c}_n(t)$ is the weight and q(t) is the spreading/scrambling code. “L” describes the number of RAKE fingers and “1/W” is the duration of one chip.

In the pipeline structure with the three pipeline stages PLS1 ... PLS3 shown, two registers RG1, RG2 are connected between the pipeline stages for data buffering because no direct pipelining is possible because of different processing speeds in the pipeline stages.

Although the present invention has been described with reference to specific embodiments, those of skill in the art will recognize that changes may be made thereto without departing from the spirit and scope of the invention as set forth in the hereafter appended claims.

ABSTRACT OF THE DISCLOSURE

To improve a rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, especially in third-generation mobile radio systems, compared with known rake receivers, such that savings with respect to the number of function blocks and logic gates used are possible, a pipeline architecture is provided in which the individual computing steps are processed as on a pipeline.

In the claims:

On page 9, cancel line 1, and substitute the following left-hand justified heading therefor:

We Claim as Our Invention:

Please cancel claims 1-9, without prejudice, and substitute the following claims therefor:

10. A rake receiver for telecommunication systems with wireless telecommunication between at least one of mobile transceivers and stationary transceivers, in third-generation mobile radio systems, comprising a pipeline architecture having a plurality of pipeline stages, wherein individual signal processing steps are processed as on a pipeline.

11. A rake receiver for telecommunication systems as claimed in claim 10, wherein there are three pipeline stages.

12. A rake receiver for telecommunication systems as claimed in claim 10, further comprising a plurality of registers for data buffering between the pipeline stages.

13. A rake receiver for telecommunication systems as claimed in claim 10, further comprising a plurality of hardware circuits in a first of the plurality of

pipeline stages, the plurality of hardware circuits able to be used in a time-division multiplex method.

14. A rake receiver for telecommunication systems as claimed in claim 10, further comprising a first hardware circuit which supports soft handover in a first of the plurality of pipeline stages.

15. A rake receiver for telecommunication systems as claimed in claim 14, further comprising a second hardware circuit which provides for code combining in the first of the plurality of pipeline stages.

16. A rake receiver for telecommunication systems as claimed in claim 15, further comprising an interpolation part which enables conjugate complex coefficients to be determined by interpolation between two channel estimations in a second of the plurality of pipeline stages.

17. A rake receiver for telecommunication systems as claimed in claim 10, wherein the pipeline architecture can be flexibly adapted to word widths and clock rates by inserting parallel processing branches.

18. A rake receiver for telecommunication systems as claimed in claim 16, further comprising a third hardware circuit which provides for low-expenditure early and late tracking of rake fingers in a third of the plurality of pipeline stages.

REMARKS

The present amendment makes editorial changes and corrects typographical errors in the specification, which includes the Abstract, in order to conform the specification to the requirements of United States Patent Practice. No new matter is added thereby. Attached hereto is a marked-up version of the

changes made to the specification by the present amendment. The attached page is captioned "**Version With Markings To Show Changes Made**".

In addition, the present amendment cancels original claims 1-9 in favor of new claims 10-18. Claims 10-18 have been presented solely because the revisions by red-lining and underlining which would have been necessary in claims 1-9 in order to present those claims in accordance with preferred United States Patent Practice would have been too extensive, and thus would have been too burdensome. The present amendment is intended for clarification purposes only and not for substantial reasons related to patentability pursuant to 35 USC §§103, 102, 103 or 112. Indeed, the cancellation of claims 1-9 does not constitute an intent on the part of the Applicants to surrender any of the subject matter of claims 1-9.

Early consideration on the merits is respectfully requested.

Respectfully submitted,

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VERSIONS WITH MARKINGS TO SHOW CHANGES MADE

In The Specification:

The Specification of the present application, including the Abstract, has been amended as follows:

SPECIFICATION

TITLE

RAKE RECEIVER FOR TELECOMMUNICATION SYSTEMS

BACKGROUND OF THE INVENTION

Field of the Invention

The present invention relates, generally, to a rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, and, more particularly, to such a rake receiver wherein a pipeline architecture having a number of pipeline stages is employed such that individual signal processing steps are processed as on a pipeline.

Description of the Prior Art

Telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers are special communication systems with an information transmission link between a message source and a message sink in which, for example, base stations and mobile parts are used as transceivers for message processing and transmission and in which:

1) the message processing and message transmission can take place in a preferred direction of transmission (simplex mode) or in both directions of transmission (duplex mode)~~,~~;

2) the message processing is preferably digital~~,~~; and

3) the message transmission via the long-distance link takes place wirelessly on the basis of various message transmission methods FDMA (Frequency Division Multiple Access), TDMA (Time Division Multiple Access) and/or CDMA (Code Division Multiple Access) - e.g., according to radio standards such as

DECT [Digital Enhanced (previously European) Cordless Telecommunication; compare *Nachrichtentechnik Elektronik 42 (1992) Jan/Feb No. 1, Berlin, DE; U. Pilger "Struktur des DECT-Standards" [Structure of the DECT standard], pages 23 to 29* in conjunction with ETSI publication *ETS 300175-1 ... 9, October 1992* and DECT publication of the *DECT Forum, February 1997, pages 1 to 16*],

GSM [Group Spéciale Mobile or Global System for Mobile Communication; compare *Informatik Spektrum 14 (1991) June, No. 3, Berlin, DE; A. Mann: "Der GSM-Standard - Grundlage für digitale europäische Mobilfunknetze" [The GSM standard - The basis for digital European mobile radio networks], pages 137 to 152* in conjunction with the publication *telekom praxis 4/1993, P. Smolka "GSM-Funkschnittstelle - Elemente und Funktionen" [GSM radio interface - elements and functions], pages 17 to 24*],

UMTS [Universal Mobile Telecommunication System; compare (1): *Nachrichtentechnik Elektronik, Berlin 45, 1995 vol. 1, pages 10 to 14 and vol. 2, pages 24 to 27; P. Jung, B. Steiner: "Konzept eines CDMA-Mobilfunksystems mit gemeinsamer Detektion für die dritte Mobilfunkgeneration" [Concept of a CDMA mobile radio system with joint detection for the third mobile radio generation];* (2): *Nachrichtentechnik Elektronik, Berlin 41, 1991, vol. 6, pages 223 to 227 and page 234; P.W. Baier, P. Jung, A. Klein: "CDMA - ein günstiges Vielfachzugriffsverfahren für frequenzselektive und zeitvariante Mobilfunkkanäle" [CDMA - an advantageous multiple access method for frequency-selective and time-variant mobile radio channels];* (3): *IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, vol. E79-A, No. 12, December 1996, pages 1 930 to 1 937; P.W. Baier, P. Jung: "CDMA Myths and Realities Revisited";* (4): *IEEE Personal Communications, February 1995, pages 38 to 47; A. Urie, M. Streeton,*

C. Mourot: "An Advanced TDMA Mobile Access System for UMTS"; (5): *telekom praxis, 5/1995, pages 9 to 14; P.W. Baier: "Spread-Spectrum-Technik und CDMA - eine ursprünglich militärische Technik erobert den zivilen Bereich" [Spread-spectrum technology and CDMA - a technology originally from the military domain conquers the civil domain];* (6): *IEEE Personal Communications, February 1995, pages 48 to 53; P.G. Andermo, L.M. Ewerbring: "A CDMA-Based Radio Access Design for UMTS";* (7): *ITG Fachberichte 124 (1993), Berlin, Offenbach: VDE Verlag ISBN 3-8007-1965-7, pages 67 to 75; Dr. T. Zimmermann, Siemens AG: "Anwendung von CDMA in der Mobilkommunikation" [Application of CDMA in mobile communication];* (8): *telcom report 16, (1993), vol. 1, pages 38 to 41; Dr. T. Ketseoglou, Siemens AG and Dr. T. Zimmermann, Siemens AG: "Effizienter Teilnehmerzugriff für die 3. Generation der Mobilkommunikation - Vielfachzugriffsverfahren CDMA macht Luftschnittstelle flexibler" [Efficient subscriber access for the 3rd-generation mobile communication - CDMA multiple access method makes the interface more flexible];* (9): *Funkschau 6/98: R. Sietmann "Ringen um die UMTS-Schnittstelle" [Tug-of-war for the UMTS interface], pages 76 to 81*] WACS or PACS, IS-54, IS-95, PHS, PDC etc. [compare IEEE Communications Magazine, January 1995, pages 50 to 57; D.D. Falconer et al.: "Time Division Multiple Access Methods for Wireless Personal Communications"].

"Message" is a higher-level term which stands both for the meaning (information) and for the physical representation (signal). In spite of identical meaning of a message, that is to say identical information, different signal forms can occur. Thus, for example, a message relating to an object can be transmitted

(1) in the form of an image,

(2) as a spoken word, <u>or</u>

(3) as a written word,

(4) as an encrypted word or image.

The type of transmission according to (1) ... (3) is normally characterized by continuous (analog) signals whereas it is usually discontinuous signals (e.g. pulses, digital signals) which are produced with the type of transmission according to (4).

In the UMTS scenario (3rd-generation mobile radio or, respectively, IMT 2000), there are two part-scenarios, for example according to the printed document *Funkschau 6/98: R. Sietmann "Ringen um die UMTS-Schnittstelle" [Tug-of-war for the UMTS interface], pages 76 to 81*. In a first part-scenario, the licensed coordinated mobile radio will be based on a WCDMA (Wideband Code Division Multiple Access) technology and operated in FDD (Frequency Division Duplex) mode as in GSM, whereas, in a second part-scenario, the unlicensed uncoordinated mobile radio will be based on a TD-CDMA (Time Division Code Division Multiple Access) technology and operated in TDD (Time Division Duplex) mode as in DECT.

For the WCDMA/FDD operation of the universal mobile telecommunication system, the air interface of the telecommunication system in each case contains a number of physical channels in the uplink and downlink direction of telecommunication in accordance with the printed document *ETSI STC SMG2 UMTS-L1, Tdoc SMG2 UMTS-L1 163/98: "UTRA Physical Layer Description FDD Parts" vers. 0.3, 1998-05-29*, of which channels a first physical channel, the so-called Dedicated Physical Control CHannel DPCCH, and a second physical channel, the so-called Dedicated Physical Data CHannel DPDCH, are shown with respect to their frame structures in FIGURES 1 and 2.

In the downlink (radio link from the base station to the mobile station) of the WCDMA/FDD system by ETSI and ARIB, respectively, the Dedicated Physical Control CHannel (DPCCH) and the Dedicated Physical Data CHannel (DPDCH) are time-division multiplexed whereas in the uplink, an I/Q multiplex is

done in which the DPDCH is transmitted in the I channel and the DPCCH is transmitted in the Q channel.

The DPCCH contains N_{pilot} pilot bits for channel estimation, N_{TPC} bits for fast power control and N_{TFI} format bits which indicate the bit rate, the type of service, the type of error protecting coding, etc. (TFI = Traffic Format Indicator).

FIGURE 3 shows, on the basis of a GSM radio scenario which includes ~~comprising~~, for example, two radio cells and Base Transceiver Stations arranged therein, a first base transceiver station BTS1 (transceiver) omnidirectionally illuminating a first radio cell FZ1 and a second base transceiver station BTS2 (transceiver) omnidirectionally illuminating a second radio cell FZ2, an FDMA/TDMA/CDMA radio scenario in which the base transceiver stations BTS1, BTS2 are connected or can be connected to a number of mobile stations MS1 ... MS5 (transceiver) located in the radio cells FZ1, FZ2 by wireless unidirectional or bi-directional-uplink UL and/or downlink DL - telecommunication on corresponding transmission channels TRC via an air interface designed for the FDMA/TDMA/CDMA radio scenario. The base transceiver stations BTS1, BTS2 are connected in at familiar manner (compare GSM telecommunication system) to a base station controller BSC which handles the frequency administration and switching functions in controlling the base transceiver stations. The base station controller BSC, in turn, is connected via a Mobile Switching Center MSC
to the higher-level telecommunication network~~,~~;e.g., the PSTN (Public Switched Telecommunication Network). The mobile switching center MSC is the administrative center for the telecommunication system shown. It handles the complete call administration and, with attached registers (not shown), the authentication of the telecommunication subscribers and the location monitoring in the network.

FIGURE 4 shows the basic configuration of the base transceiver station BTS1, BTS2 constructed as transceiver and FIGURE 5 shows the basic

configuration of the mobile station MS1 ... MS5, also constructed as transceiver. The base transceiver station BTS1, BTS2 handles the transmitting and receiving of radio messages from and to the mobile station MS1 ... MS5 and the mobile station MTS1 ... MTS5 handles the transmitting and receiving of radio messages from and to the base transceiver station BTS1, BTS2. For this purpose, the base station has a transmitting antenna SAN and a receiving antenna EAN and the mobile station MS1 ... MS5 has a common antenna ANT for transmitting and receiving which is controllable by an antenna switch AU. In the uplink (receiving path), the base transceiver station BTS1, BTS2 receives via the receiving antenna EAN, for example, at least one radio message FN with an FDMA/TDMA/CDMA component from at least one of the mobile stations MS1 ... MS5 and the mobile station MS1 ... MS5 receives in the downlink (receiving path) via the common antenna ANT, for example, at least one radio message FN with an FDMA/TDMA/CDMA component from at least one base transceiver station BTS1, BTS2. The radio message FN consists of a broadband spread-spectrum carrier signal modulated with an information item composed of data symbols.

In a radio receiver FEE, the received carrier signal is filtered and mixed down to an intermediate frequency which, in turn, is thereafter sampled and quantized. After analog/digital conversion, the signal, which has been distorted by multipath propagation on the radio path, is supplied to an equalizer EQL which largely equalizes (synchronizes) the distortions.

After that, a channel estimator KS attempts to estimate the transmission characteristics of the transmission channel TRC on which the radio message FN has been transmitted. The transmission characteristics of the channel are specified by the channel impulse response in the time domain. To be able to estimate the channel impulse response, a special supplementary information item in the form of a so-called midamble, which is designed as training information sequence, is assigned or allocated to the radio message FN at the transmitting end (by the

mobile station MS1 ... MS5 or, respectively, the base transceiver station BTS1, BTS2, in the present case).

The individual mobile-station-specific signal components, which are contained in the common signal, are equalized and separated in a known manner in a subsequent data detector DD which is common to all received signals. After the equalization and separation, the data symbols hitherto present are converted into binary data in a symbol-to-data converter SDW. After that, the original bit stream is obtained from the intermediate frequency in a demodulator DMOD before the individual time slots are allocated to the correct logical channels~~,~~ and, thus, also to the different mobile stations~~,~~ in a demultiplexer DMUX.

The bit sequence obtained is decoded channel by channel in a channel codec KC. Depending on the channel, the bit information is allocated to the control and signaling timeslot or to a voice timeslot and, in the case of the base transceiver station (FIGURE 4), the control and signaling data and the voice data are jointly transferred to an interface SS responsible for the signaling and voice coding/decoding (voice codec) for transmission to the base station controller BSC. ~~whereas—in~~ In the case of the mobile station (FIGURE 5), the control and signaling data are transferred to a control and signaling unit STSE responsible for the complete signaling and control of the mobile station and the voice data are transferred to a voice codec SPC designed for voice input and output.

In the voice codec of the interface SS in the base transceiver station BTS1, BTS2, the voice data ~~[lacuna]~~ is in a predetermined data stream (e.g., 64-kbit/s stream in the direction of the network and 13 kbit/s stream in the direction from the network).

The complete control of the base transceiver station BTS1, BTS2 is performed in a control unit STE.

In the downlink (transmitting path), the base transceiver station BTS1, BTS2 sends via the transmitting antenna SAN, for example, at least one radio message FN with an FDMA/TDMA/CDMA component to at least one of the

mobile stations MS1 ... MS5 and the mobile station MS1 ... MS5 sends in the uplink (transmitting path) via the common antenna ANT, for example, at least one radio message FN with an FDMA/TDMA/CDMA component to at least one base transceiver station BTS1, BTS2.

The transmitting path begins at the base transceiver station BTS1, BTS2 in FIGURE 4, by control and signaling data and voice data received by the base station controller BSC via the interface SS being assigned to a control and signaling timeslot or a voice timeslot in the channel codec KC and these timeslots being coded in a bit sequence channel by channel.

The transmitting path begins in the case of the mobile station MS1 ... MS5 in FIGURE 5 by voice data received from the voice codec SPC and control and signaling data received from the control and signaling unit STSE being assigned to a control and signaling timeslot or a voice timeslot in the channel codec KC and these timeslots being coded in a bit sequence channel by channel.

The bit sequence obtained in the base station BTS1, BTS2 and in the mobile station MS1 ... MS5 is, in each case, converted into data symbols in a data-to-symbol converter DSW. Following this, the data symbols are, in each case, spread with ~~an in each case~~ a subscriber-associated code in a spreader SPE. In the burst generator BG consisting of a burst assembler BZS and a multiplexer MUX, a training information sequence in the form of a midamble is then added ~~in each case~~ to the spread data symbols in the burst assembler BZS for channel estimation and the burst information obtained in this manner is set to the correct timeslot ~~in each case~~ in the multiplexer MUX. The burst obtained is then radio-frequency modulated, in each case, in a modulator MOD and digital/analog converted before the signal obtained in this manner is radiated at the transmitting antenna SAN or, respectively, the common antenna ANT via a radio transmitter FSE as radio message FN.

In CDMA-based systems, the problem of multiple reception, the so-called delay spread, in the presence of echoes can be solved in spite of the great

bandwidth and the very small chip or bit times of these systems by combining the received signals with one another in order to increase the reliability of detection. Naturally, the channel characteristics must be known for this. To determine these, a pilot sequence common to all subscribers is used (compare FIGURES 1 and 2) which is additionally radiated independently and with increased transmitting power without modulation by a message sequence. Its reception provides the receiver with the information on how many paths are involved in the instantaneous situation of reception and what delay times are produced.

In a RAKE receiver, the signals coming in via the individual paths are acquired in separate receivers, the "fingers" of the RAKE receiver, detected and added together in an addition section weighted among each other after compensation for the delay times and the phase shifts of the echoes.

A RAKE receiver is used, in particular, for recovering digital data from a radio reception signal having a CDMA component. The signals superimposed via multipath propagation and distorted by the channel are recovered and the symbol energies of the individual propagation paths are accumulated.

The theory for the RAKE receiver has been sufficiently well investigated and is known (compare J.G. Proakis: "Digital Communications"; McGraw-Hill, Inc.; 3rd edition, 1995; pp. 728 to 739 and K.D. Kammeyer: "Nachrichtenübertragung" [Information transmission]; B:G. Teubner Stuttgart, 1996; pp. 658 to 669).

~~The~~ An object ~~forming the basis~~ of the present invention ~~consists in specifying~~ is to specify a rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, especially in third-generation mobile radio systems, which exhibits a smaller number of function blocks and/or logic gates compared with known rake receivers.

~~This object is achieved by the features of claim 1.~~

SUMMARY OF THE INVENTION

Accordingly, the present invention has ~~The idea forming the basis of the invention consists in that~~ a pipeline architecture, ~~comprising~~ including a number of pipeline stages (pipeline structure), which is implemented ~~in which~~ such that the individual signal processing steps or computing steps are processed as in a pipeline. As a result, the hardware circuits used ~~in accordance with claim 3, in particular,~~ can be used in time-division multiplex.

In an embodiment ~~According to claim 2~~, it is ~~of advantage~~ advantageous to use three pipeline stages. ~~According to claim 3, it~~ It is also advantageous to buffer the processing in the pipeline stages ~~by means of~~ via two registers if no direct pipelining is possible in the three pipeline stages because of different processing speeds in the pipeline stages.

In a first pipeline stage, the data (e.g., chips or subchips in the case of oversampling) are read out of a memory (e.g., a dual-port RAM (DP-RAM)). To be able to superimpose the symbols of the individual signal paths in the correct phase (code combining), the corresponding path delays must be taken into consideration. The addresses are also calculated in the first stage. The delay time is added to the current address in the form of an offset. There are, for example, "L" offsets, "L" corresponding to the number of fingers in the RAKE receiver and a different offset being needed in each clock period. Thus, the memory is accessed in every clock period.

Furthermore, the code generated by at least one code generator, the spreading code and/or the scrambling code required for descrambling, is multiplied by the current value from the dual-port RAM in the first pipeline stage. This operation is relatively simple since it only consists of sign operations and of two additional additions in the case of complex scrambling codes.

In addition, the soft handover is handled in the first pipeline stage. In the case of a soft handover, the RAKE receiver receives signals which have been sent with different scrambling and spreading codes from, for example, base transceiver

stations. The maximum possible number of RAKE fingers must be shared out among the base transceiver stations in accordance with the quality of reception. For this reason, the code generators are switched in dependence on the RAKE fingers. The multiplexer performing the switching operates at a maximum of L* W MHz. To increase the number of base transceiver stations, further code generators can be added.

In the second pipeline stage, each value is multiplied by a weight. These weights are different for each finger and change with every clock period. In principle, they are repeated after "L" steps. In the case of an interpolation, the delta values are accumulated to form the weights.

In the last, third pipeline stage, the chip energies of the individual RAKE fingers are accumulated to form the symbol energy U_{symb}.

$$u_{symb} = \sum_{i=1}^{SF} \sum_{j=1}^{L} u_{ij}$$; where SF = spreading factor, L = number of RAKE fingers.

Additional features and advantages of the present invention are described in, and will be apparent from, the Detailed Description of the Preferred Embodiments and the Drawings.

~~Advantages and special features of the RAKE pipeline architecture~~

DESCRIPTION OF THE DRAWINGS

Figures 1 and 2 show, for the WCDMA/FDD operation of the universal mobile telecommunication system, the Dedicated Physical Control Channel and the Dedicated Physical Data Channel of the air interface of a telecommunication system with respect to their frame structures;

Figure 3 shows, on the basis of the GSM radio scenario, first and second base transceiver stations;

Figure 4 shows the basic configuration of the base transceiver station constructed as a transceiver;

Figure 5 shows the basic configuration of the mobile station constructed as a transceiver; and

Figure 6 shows, in block diagram form, rake receivers having a pipeline architecture.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

1. Time-division multiplexing of the RAKE pipeline architecture

In the known architectures, each finger of the RAKE receiver is implemented individually, the chips are accumulated to form symbols and, at the end, the sum over all fingers is formed. In the case of "L" fingers, this leads to the following hardware requirement:

- $L + 1$ adder and
- $2 * L$ multipliers (complex multiplication)

If the signal processing chain for a RAKE finger is set up as a pipeline, a single pipelined RAKE finger can emulate a complete RAKE receiver in time-division multiplex. This is only limited by the number of fingers and maximum clock rate of the available technology. This reduces the complexity to

- 1 adder,
- 2 multipliers and
- b+2*m additional registers,

where "b" is the maximum number of base transceiver stations involved in the soft handover and "m" is the number of fingers to be corrected for the early-late tracking.

2. Code combing via dual-port RAM accesses

To be able to superimpose the symbols of the individual signal paths in the correct phase (code combining), the corresponding path delays must be taken into consideration. Various known approaches to a solution use shift registers and a relatively elaborate multiplexer logic for this purpose.

In the proposed approach to the solution, a simple dual-port RAM (DP-RAM) is used. Code combining is done by selectively using address offsets which correspond to the delay between the different propagation paths.

Instead of the dual-port RAM, SRAMs, SDRAMs or SSRAMs can also be used which emulate a DP-RAM.

3. Interpolation of the weights

To reduce the number of channel estimations for calculating the conjugate complex coefficients (weights) or, respectively, to achieve a smaller time deviation from the ideal value of these estimations, it is possible to determine the coefficients between two estimations ~~by means of~~ via *interpolation*. This simplification in the channel estimation can be easily integrated into the pipeline architecture.

4. Early-late tracking of the RAKE fingers

The prerequisite for acceptable bit error rates is to position the RAKE fingers as accurately as possible. The position of the individual RAKE fingers is determined with the aid of an elaborate matched filter. The length of the channel, the required accuracy in the positioning of the fingers and the frequency of calculations performed determine the expenditure for the matched filter. Any more inaccurate, initial determination of the finger position performed at greater time intervals lead to a considerable reduction in the expenditure for the matched filter. To counteract the resultant degradation, the so-called early/late tracking is

used. The early finger is positioned one half chip before, and the late finger one half chip after, the RAKE finger to be positioned (main finger). The energies of the early and late finger are calculated in the last stage of the RAKE receiver and only require little complexity. If the energies of the two fingers ≈ 0, i.e. they have approximately the same low energy, the enclosed finger, ~~The~~ the main finger, has an almost optimum position. If the energies of the tracking fingers are not approximately equal or ≠ 0, a repositioning takes place at W/n intervals, "W" being the chip frequency and "n" being the oversampling rate.

5. Soft handover

In the soft handover, the RAKE receiver receives signals sent with different scrambling/spreading codes from a number of base transceiver stations. The maximum number of RAKE fingers must be shared among the base transceiver stations in accordance with the quality of reception. This requires switching of the code generators which is dependent on the RAKE fingers. The multiplexer performing the switching operates with a maximum of L * W MHz, taking into consideration the early/late fingers.

During the soft handover, the base transceiver stations involved send the same user data to the mobile station. To control the transmitting power of the mobile station, the latter additionally receives an information item, the so-called TPC (transfer power control, compare FIGURES 1 and 2) bit which says whether the transmitting power has to be increased or decreased. For this reason, the different base transceiver station-dependent TPC bits must be decoded. The concluding or last part of the processing pipeline accumulates for this purpose symbols representing TPC bits, separated in accordance with received base transceiver station.

6. Flexibility of the architecture with respect to word widths, clock rates and parallel processing

Depending on the field of application and the quality required (e.g., bit error rate) of the communication link (data, voice, etc.), a different number of RAKE fingers and word widths are required in the signal processing path. The proposed architecture allows simple adaptation. Greater word widths require lower clock rates of the individual processing units, the technology remaining the same. The processing power of the RAKE pipeline architecture can be increased by inserting parallel processing branches without greatly increasing circuit complexity. This provides for higher clock rates.

In the implementation of a RAKE receiver in hardware and/or software, however, savings can be achieved with respect to the number of function blocks used or their complexity by suitable mapping in software and hardware, and a greater flexibility in the parametrization; e.g., number of RAKE fingers.

In addition, the availability of fast technologies in the field of chip design (e.g., ASIC, FPGA) allows essential parts of the hardware to be used in time-division multiplex and, thus, to reduce the necessary number of logic gates.

An exemplary embodiment of the present invention is explained with reference to FIGURE 6.

~~FIGURE 6 shows the pipeline architecture of a RAKE receiver in a block diagram.~~

FIGURE 6 shows, in block diagram form, RAKE receivers having a pipeline architecture, consisting of three pipeline stages, a first pipeline stage PLS1, a second pipeline stage PLS2 and a third pipeline stage PLS3 for L=8 fingers, soft handover with two base transceiver stations and early/late tracking. The pipeline structure shown is designed for one finger, but all fingers can be successively corrected. The clock rates specified relate to the RAKE receiver thus specified and are, therefore, a multiple of the chip frequency of 4 096 MChip. The word widths specified within the signal processing chain are derived from the boundary conditions for UMTS standardization (compare *SMG2 UMTSPhysical*

Layer Expert Group: "UTRA Physical Layer Description FDD Parts" vers. 0.4, 1998-06-25).

In principle, the architecture described can be extended to a different chip frequency "W", to any number of fingers "L", to "b" possible base transceiver stations in the case of a soft handover and 2*L fingers for the early/late tracking. Similarly, the architecture is flexible with respect to the choice of word widths used in the signal processing path.

The received signal r(t) is written in a dual-port RAM (DP-RAM) DPR with a frequency of 4 096 * n MHz (where n is the oversampling rate). The addresses for storing input data (chips) in the dual-port RAM DPR are generated by a first address counter AZ1.

To read the received chips out of the dual-port RAM DPR, an address (8 * 4 096 MHz clock) is calculated from the addition of a free-running second address counter AZ2 and the offsets dependent on the RAKE finger. The offsets are located in offset registers. For the early/late finger tracking to be implemented, two of the offset registers can be used for positioning the early and late finger. To recover the symbols, the data read out ~~are~~ is multiplied in a first multiplier MUL1 by a spreading code generated by at least one code generator (two code generators CG1, CG2 in FIGURE 6) and/or a scrambling code required for descrambling. In the case of simple codes, this is a sign operation whereas an additional addition is added in the case of complex codes.

In the soft handover case, the RAKE receiver receives signals sent with different scrambling/spreading codes from, e.g., two base transceiver stations, base transceiver station 1 and base transceiver station 2. The maximum possible number of RAKE fingers must be shared among the base transceiver stations in accordance with the quality of reception. The scrambling/spreading codes are selected in a code combining/soft handover circuit CCSHS. This is why the code generators CG1, CG2 need to be switched in dependence on the RAKE fingers. A multiplexer

MUX which performs the switching operates with a maximum of 8 * 4 096 MHz in this example. In addition, the corresponding path delays are taken into consideration in this circuit CCSHS to be able to obtain a superimposition of the symbols of the individual signal paths in the correct phase (code combining).

The channel necessary for transmission distorts the signal. In the second pipeline stage PLS2, the channel estimator calculates the conjugate complex channel coefficients (weights) necessary for correcting the distortion from the received pilot sequence. The receiver, therefore, multiplies the recovered symbols of the individual RAKE fingers by their weights C_i* in a second multiplier MUL2. These weights are stored in a ring memory.

To avoid frequent estimation of the channel because this is a computationally intensive process, and to achieve a smaller time deviation of the coefficients from the ideal value, the weights are interpolated between two estimations in interpolation ~~means~~ part IPM. This results in continual adding together of delta values.

In the last pipeline stage, the third pipeline stage PLS3, the chip energies of the individual fingers and thus the levels belonging to a symbol are accumulated in an accumulator AK successively over the period of one symbol. Symbols which represent TPC (transfer power control) bits must be accumulated separated by received base transceiver station. After each symbol, the accumulator AK must be reset.

For the early/late tracking, two separate accumulator registers AKR must be additionally provided for each early/late finger.

For each timeslot, overflow detector ÜD registers a bit overflow produced and deletes it at the beginning of the new timeslot.

If an overflow occurs, an AGC control ACGR must be informed that the input gain must be decreased.

The estimated value of the symbol $\underline{U}_m$ is present at the output of the RAKE receiver.

The following expression represents the general calculation of the estimated value $\underline{U}_m$ of a received symbol:

$$\underline{\quad}_m = \int_0^T \underline{r}(t) * \sum_{n=1}^{L} \underline{c}_n(t) * \underline{q}(t - n / W)dt$$

where r(t) is the received signal, $\underline{c}_n(t)$ is the weight and q(t) is the spreading/scrambling code. “L” describes the number of RAKE fingers and “1/W” is the duration of one chip.

In the pipeline structure with the three pipeline stages PLS1 ... PLS3 shown, two registers RG1, RG2 are connected between the pipeline stages for data buffering because no direct pipelining is possible because of different processing speeds in the pipeline stages.

Although the present invention has been described with reference to specific embodiments, those of skill in the art will recognize that changes may be made thereto without departing from the spirit and scope of the invention as set forth in the hereafter appended claims.

ABSTRACT OF THE DISCLOSURE

~~Rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, especially in third-generation mobile radio systems~~

To improve a rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, especially in third-generation mobile radio systems, compared with known rake receivers, ~~in~~ such ~~a manner~~ that savings with respect to the number of function blocks and logic gates used are possible, a pipeline architecture is provided in which the individual computing steps are processed as on a pipeline.

~~Figure 6~~

Description

Rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, especially in third-generation mobile radio systems

Telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers are special communication systems with an information transmission link between a message source and a message sink in which, for example, base stations and mobile parts are used as transceivers for message processing and transmission and in which

1) the message processing and message transmission can take place in a preferred direction of transmission (simplex mode) or in both directions of transmission (duplex mode),
2) the message processing is preferably digital,
3) the message transmission via the long-distance link takes place wirelessly on the basis of various message transmission methods FDMA (Frequency Division Multiple Access), TDMA (Time Division Multiple Access) and/or CDMA (Code Division Multiple Access) - e.g. according to radio standards such as

   DECT [Digital Enhanced (previously European) Cordless Telecommunication; compare *Nachrichtentechnik Elektronik 42 (1992) Jan/Feb No. 1, Berlin, DE; U. Pilger "Struktur des DECT-Standards" [Structure of the DECT standard], pages 23 to 29* in conjunction with ETSI publication *ETS 300175-1 ... 9, October 1992* and DECT publication of the *DECT Forum, February 1997, pages 1 to 16*],

   GSM [Group Spéciale Mobile or Global System for Mobile Communication; compare *Informatik Spektrum 14 (1991) June, No. 3, Berlin, DE; A. Mann: "Der*

GSM-Standard - Grundlage für digitale europäische Mobilfunknetze" [The GSM standard - The basis for digital European mobile radio networks], pages 137 to 152 in conjunction with the publication *telekom praxis*

4/1993, P. Smolka "GSM-Funkschnittstelle - Elemente und Funktionen" [GSM radio interface - elements and functions], pages 17 to 24], UMTS [Universal Mobile Telecommunication System; compare (1): Nachrichtentechnik Elektronik, Berlin 45, 1995 vol. 1, pages 10 to 14 and vol. 2, pages 24 to 27; P. Jung, B. Steiner: "Konzept eines CDMA-Mobilfunksystems mit gemeinsamer Detektion für die dritte Mobilfunkgeneration" [Concept of a CDMA mobile radio system with joint detection for the third mobile radio generation]; (2): Nachrichtentechnik Elektronik, Berlin 41, 1991, vol. 6, pages 223 to 227 and page 234; P.W. Baier, P. Jung, A. Klein: "CDMA - ein günstiges Vielfachzugriffsverfahren für frequenzselektive und zeitvariante Mobilfunkkanäle" [CDMA - an advantageous multiple access method for frequency-selective and time-variant mobile radio channels]; (3): IEICE Transactions on Fundamentals of Electronics, Communications and Computer Sciences, vol. E79-A, No. 12, December 1996, pages 1 930 to 1 937; P.W. Baier, P. Jung: "CDMA Myths and Realities Revisited"; (4): IEEE Personal Communications, February 1995, pages 38 to 47; A. Urie, M. Streeton, C. Mourot: "An Advanced TDMA Mobile Access System for UMTS"; (5): telekom praxis, 5/1995, pages 9 to 14; P.W. Baier: "Spread-Spectrum-Technik und CDMA - eine ursprünglich militärische Technik erobert den zivilen Bereich" [Spread-spectrum technology and CDMA - a technology originally from the military domain conquers the civil domain]; (6): IEEE Personal Communications, February 1995, pages 48 to 53; P.G. Andermo, L.M. Ewerbring: "A CDMA-Based Radio Access Design for UMTS"; (7): ITG Fachberichte 124 (1993), Berlin, Offenbach: VDE Verlag ISBN 3-8007-1965-7, pages 67 to 75; Dr. T. Zimmermann, Siemens AG: "Anwendung von CDMA

in der Mobilkommunikation" [Application of CDMA in mobile communication]; (8): *telcom report 16, (1993), vol. 1, pages 38 to 41; Dr. T. Ketseoglou, Siemens AG and Dr. T. Zimmermann, Siemens AG: "Effizienter Teilnehmerzugriff für die 3. Generation der Mobilkommunikation - Vielfachzugriffsverfahren CDMA macht Luftschnittstelle flexibler" [Efficient subscriber access for the 3rd-generation mobile communication - CDMA multiple access method makes the interface more flexible];* (9): *Funkschau 6/98: R. Sietmann "Ringen um die UMTS-Schnittstelle" [Tug-of-war for the UMTS interface], pages 76 to 81*] WACS or PACS, IS-54, IS-95, PHS, PDC etc. [compare IEEE Communications Magazine, January 1995, pages 50 to 57; D.D. Falconer et al.: "Time Division Multiple Access Methods for Wireless Personal Communications"].

"Message" is a higher-level term which stands both for the meaning (information) and for the physical representation (signal). In spite of identical meaning of a message - that is to say identical information - different signal forms can occur. Thus, for example, a message relating to an object can be transmitted

(1) in the form of an image,
(2) as a spoken word,
(3) as a written word,
(4) as an encrypted word or image.

The type of transmission according to (1) ... (3) is normally characterized by continuous (analog) signals whereas it is usually discontinuous signals (e.g. pulses, digital signals) which are produced with the type of transmission according to (4).

In the UMTS scenario (3rd-generation mobile radio or, respectively, IMT 2000), there are two part-scenarios, for example according to the printed document *Funkschau 6/98: R. Sietmann "Ringen um die UMTS-Schnittstelle" [Tug-of-war for the UMTS interface], pages 76 to 81*. In a first part-scenario, the licensed coordinated mobile radio will be based on a WCDMA (Wideband Code Division Multiple Access) technology and operated in FDD (Frequency Division Duplex) mode as in GSM whereas, in a second part-scenario, the unlicensed uncoordinated mobile radio will be based on a TD-CDMA (Time Division Code Division Multiple Access) technology and operated in TDD (Time Division Duplex) mode as in DECT.

For the WCDMA/FDD operation of the universal mobile telecommunication system, the air interface of the telecommunication system in each case contains a number of physical channels in the uplink and downlink direction of telecommunication in accordance with the printed document *ETSI STC SMG2 UMTS-L1, Tdoc SMG2 UMTS--L1 163/98: "UTRA Physical Layer Description FDD Parts" vers. 0.3, 1998-05-29*, of which channels a first physical channel, the so-called Dedicated

Physical Control CHannel DPCCH, and a second physical channel, the so-called Dedicated Physical Data CHannel DPDCH, are shown with respect to their frame structures in FIGURES 1 and 2.

In the downlink (radio link from the base station to the mobile station) of the WCDMA/FDD system by ETSI and ARIB, respectively, the Dedicated Physical Control CHannel (DPCCH) and the Dedicated Physical Data CHannel (DPDCH) are time-division multiplexed whereas in the uplink, an I/Q multiplex is done in which the DPDCH is transmitted in the I channel and the DPCCH is transmitted in the Q channel.

The DPCCH contains N_{pilot} pilot bits for channel estimation, N_{TPC} bits for fast power control and N_{TFI} format bits which indicate the bit rate, the type of service, the type of error protecting coding, etc. (TFI = Traffic Format Indicator).

FIGURE 3 shows, on the basis of a GSM radio scenario comprising, for example, two radio cells and Base Transceiver Stations arranged therein, a first base transceiver station BTS1 (transceiver) omni-directionally illuminating a first radio cell FZ1 and a second base transceiver station BTS2 (transceiver) omnidirectionally illuminating a second radio cell FZ2, an FDMA/TDMA/CDMA radio scenario in which the base transceiver stations BTS1, BTS2 are connected or can be connected to a number of mobile stations MS1 ... MS5 (transceiver) located in the radio cells FZ1, FZ2 by wireless unidirectional or bi-directional-uplink UL and/or downlink DL - telecommunication on corresponding transmission channels TRC via an air interface designed for the FDMA/TDMA/CDMA radio scenario. The base transceiver stations BTS1, BTS2 are connected in at familiar manner (compare GSM telecommunication system) to a base station controller BSC which handles the frequency administration and switching functions in controlling the base transceiver stations. The base station controller BSC, in turn, is connected via a Mobile Switching Center MSC

to the higher-level telecommunication network, e.g. the PSTN (Public Switched Telecommunication Network). The mobile switching center MSC is the administrative center for the telecommunication system shown. It handles the complete call administration and, with attached registers (not shown), the authentication of the telecommunication subscribers and the location monitoring in the network.

FIGURE 4 shows the basic configuration of the base transceiver station BTS1, BTS2 constructed as transceiver and FIGURE 5 shows the basic configuration of the mobile station MS1 ... MS5, also constructed as transceiver. The base transceiver station BTS1, BTS2 handles the transmitting and receiving of radio messages from and to the mobile station MS1 ... MS5 and the mobile station MTS1 ... MTS5 handles the transmitting and receiving of radio messages from and to the base transceiver station BTS1, BTS2. For this purpose, the base station has a transmitting antenna SAN and a receiving antenna EAN and the mobile station MS1 ... MS5 has a common antenna ANT for transmitting and receiving which is controllable by an antenna switch AU. In the uplink (receiving path), the base transceiver station BTS1, BTS2 receives via the receiving antenna EAN, for example, at least one radio message FN with an FDMA/TDMA/CDMA component from at least one of the mobile stations MS1 ... MS5 and the mobile station MS1 ... MS5 receives in the downlink (receiving path) via the common antenna ANT, for example, at least one radio message FN with an FDMA/TDMA/CDMA component from at least one base transceiver station BTS1, BTS2. The radio message FN consists of a broadband spread-spectrum carrier signal modulated with an information item composed of data symbols.

In a radio receiver FEE, the received carrier signal is filtered and mixed down to an intermediate frequency which, in turn, is thereafter sampled and quantized. After analog/digital conversion, the

signal, which has been distorted by multipath propagation on the

radio path, is supplied to an equalizer EQL which largely equalizes (synchronizes) the distortions.

After that, a channel estimator KS attempts to estimate the transmission characteristics of the transmission channel TRC on which the radio message FN has been transmitted. The transmission characteristics of the channel are specified by the channel impulse response in the time domain. To be able to estimate the channel impulse response, a special supplementary information item in the form of a so-called midamble, which is designed as training information sequence, is assigned or allocated to the radio message FN at the transmitting end (by the mobile station MS1 ... MS5 or, respectively, the base transceiver station BTS1, BTS2, in the present case).

The individual mobile-station-specific signal components, which are contained in the common signal, are equalized and separated in a known manner in a subsequent data detector DD which is common to all received signals. After the equalization and separation, the data symbols hitherto present are converted into binary data in a symbol-to-data converter SDW. After that, the original bit stream is obtained from the intermediate frequency in a demodulator DMOD before the individual time slots are allocated to the correct logical channels, and thus also to the different mobile stations, in a demultiplexer DMUX.

The bit sequence obtained is decoded channel by channel in a channel codec KC. Depending on the channel, the bit information is allocated to the control and signaling timeslot or to a voice timeslot and - in the case of the base transceiver station (FIGURE 4) - the control and signaling data and the voice data are jointly transferred to an interface SS responsible for the signaling and voice coding/decoding (voice codec) for transmission to the base station controller BSC whereas - in the case of the mobile station (FIGURE 5) - the

control and signaling data are transferred to a control and signaling unit STSE responsible for the complete signaling and control of the mobile station and the voice data are transferred to a voice codec SPC designed for voice input and output.

In the voice codec of the interface SS in the base transceiver station BTS1, BTS2, the voice data [lacuna] in a predetermined data stream (e.g. 64-kbit/s stream in the direction of the network and 13 kbit/s stream in the direction from the network).

The complete control of the base transceiver station BTS1, BTS2 is performed in a control unit STE.

In the downlink (transmitting path), the base transceiver station BTS1, BTS2 sends via the transmitting antenna SAN, for example, at least one radio message FN with an FDMA/TDMA/CDMA component to at least one of the mobile stations MS1 ... MS5 and the mobile station MS1 ... MS5 sends in the uplink (transmitting path) via the common antenna ANT, for example, at least one radio message FN with an FDMA/TDMA/CDMA component to at least one base transceiver station BTS1, BTS2.

The transmitting path begins at the base transceiver station BTS1, BTS2 in FIGURE 4, by control and signaling data and voice data received by the base station controller BSC via the interface SS being assigned to a control and signaling timeslot or a voice timeslot in the channel codec KC and these timeslots being coded in a bit sequence channel by channel.

The transmitting path begins in the case of the mobile station MS1 ... MS5 in FIGURE 5 by voice data received from the voice codec SPC and control and signaling data received from the control and signaling unit STSE being assigned to a control and signaling timeslot or a voice

timeslot in the channel codec KC and these timeslots being coded in a bit sequence channel by channel.

The bit sequence obtained in the base station BTS1, BTS2 and in the mobile station MS1 ... MS5 is in each case converted into data symbols in a data-to-symbol converter DSW. Following this, the data symbols are in each case spread with an in each case subscriber-associated code in a spreader SPE. In the burst generator BG consisting of a burst assembler BZS and a multiplexer MUX, a training information sequence in the form of a midamble is then added in each case to the spread data symbols in the burst assembler BZS for channel estimation and the burst information obtained in this manner is set to the correct timeslot in each case in the multiplexer MUX. The burst obtained is then radio-frequency modulated in each case in a modulator MOD and digital/analog converted before the signal obtained in this manner is radiated at the transmitting antenna SAN or, respectively, the common antenna ANT via a radio transmitter FSE as radio message FN.

In CDMA-based systems, the problem of multiple reception, the so-called delay spread, in the presence of echoes can be solved in spite of the great bandwidth and the very small chip or bit times of these systems by combining the received signals with one another in order to increase the reliability of detection. Naturally, the channel characteristics must be known for this. To determine these, a pilot sequence common to all subscribers is used (compare FIGURES 1 and 2) which is additionally radiated independently and with increased transmitting power without modulation by a message sequence. Its reception provides the receiver with the information on how many paths are involved in the instantaneous situation of reception and what delay times are produced.

In a RAKE receiver, the signals coming in via the individual paths are acquired in separate receivers, the "fingers" of the RAKE receiver, detected and added together in an addition section weighted among each other after compensation for the delay times and the phase shifts of the echoes.

A RAKE receiver is used, in particular, for recovering digital data from a radio reception signal having a CDMA component. The signals superimposed via multipath propagation and distorted by the channel are recovered and the symbol energies of the individual propagation paths are accumulated.

The theory for the RAKE receiver has been sufficiently well investigated and is known (compare J.G. Proakis: "Digital Communications"; McGraw-Hill, Inc.; 3rd edition, 1995; pp. 728 to 739 and K.D. Kammeyer: "Nachrichtenübertragung" [Information transmission]; B:G. Teubner Stuttgart, 1996; pp. 658 to 669).

The object forming the basis of the invention consists in specifying a rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, especially in third-generation mobile radio systems, which exhibits a smaller number of function blocks and/or logic gates compared with known rake receivers.

This object is achieved by the features of claim 1.

The idea forming the basis of the invention consists in that a pipeline architecture comprising a number of pipeline stages (pipeline structure) is implemented in which the individual signal processing steps or computing steps are processed as in a pipeline. As a result, the hardware

circuits used in accordance with claim 3, in particular, can be used in time-division multiplex.

According to claim 2, it is of advantage to use three pipeline stages. According to claim 3, it is advantageous to buffer the processing in the pipeline stages by means of two registers if no direct pipelining is possible in the three pipeline stages because of different processing speeds in the pipeline stages.

In a first pipeline stage, the data - e.g. chips or subchips in the case of oversampling - are read out of a memory - e.g. a dual-port RAM (DP-RAM). To be able to superimpose the symbols of the individual signal paths in the correct phase (code combining), the corresponding path delays must be taken into consideration. The addresses are also calculated in the first stage. The delay time is added to the current address in the form of an offset. There are, for example, "L" offsets, "L" corresponding to the number of fingers in the RAKE receiver and a different offset being needed in each clock period. Thus, the memory is accessed in every clock period.

Furthermore, the code generated by at least one code generator, the spreading code and/or the scrambling code required for descrambling, is multiplied by the current value from the dual-port RAM in the first pipeline stage. This operation is relatively simple since it only consists of sign operations and of two additional additions in the case of complex scrambling codes.

In addition, the soft handover is handled in the first pipeline stage. In the case of a soft handover, the RAKE receiver receives signals which have been sent with different scrambling and spreading codes from, for example, base transceiver stations. The maximum possible number of RAKE

fingers must be shared out among the base transceiver stations in accordance with the quality of reception. For this reason, the code generators are switched in dependence on the RAKE fingers. The multiplexer performing the switching operates at a maximum of L* W MHz. To increase the number of base transceiver stations, further code generators can be added.

In the second pipeline stage, each value is multiplied by a weight. These weights are different for each finger and change with every clock period. In principle, they are repeated after "L" steps. In the case of an interpolation, the delta values are accumulated to form the weights.

In the last, third pipeline stage, the chip energies of the individual RAKE fingers are accumulated to form the symbol energy U_{symb}.

$u_{symb} = \sum_{i=1}^{SF} \sum_{j=1}^{L} u_{ij}$; where SF = spreading factor, L = number of RAKE fingers.

Advantages and special features of the RAKE pipeline architecture

1. Time-division multiplexing of the RAKE pipeline architecture

In the known architectures, each finger of the RAKE receiver is implemented individually, the chips are accumulated to form symbols and, at the end, the sum over all fingers is formed. In the case of "L" fingers, this leads to the following hardware requirement:

- L + 1 adder and
- 2 * L multipliers (complex multiplication)

If the signal processing chain for a RAKE finger is set up as a pipeline, a single pipelined RAKE finger can emulate a complete RAKE receiver in time-division multiplex. This is only limited by the number of fingers and maximum clock rate of the available technology. This reduces the complexity to

- 1 adder,
- 2 multipliers and
- b+2*m additional registers,

where "b" is the maximum number of base transceiver stations involved in the soft handover and "m" is the number of fingers to be corrected for the early-late tracking.

2. Code combing via dual-port RAM accesses

To be able to superimpose the symbols of the individual signal paths in the correct phase (code combining), the corresponding path delays must be taken into consideration. Various known approaches to a solution use shift registers and a relatively elaborate multiplexer logic for this purpose.

In the proposed approach to the solution, a simple dual-port RAM (DP-RAM) is used. Code combining is done by selectively using address offsets which correspond to the delay between the different propagation paths.

Instead of the dual-port RAM, SRAMs, SDRAMs or SSRAMs can also be used which emulate a DP-RAM.

3. Interpolation of the weights

To reduce the number of channel estimations for calculating the conjugate complex coefficients (weights) or,

respectively, to achieve a smaller time deviation from the ideal value of these estimations, it is possible to determine the coefficients between two estimations by means of interpolation. This simplification in the channel estimation can be easily integrated into the pipeline architecture.

4. Early-late tracking of the RAKE fingers

The prerequisite for acceptable bit error rates is to position the RAKE fingers as accurately as possible. The position of the individual RAKE fingers is determined with the aid of an elaborate matched filter. The length of the channel, the required accuracy in the positioning of the fingers and the frequency of calculations performed determine the expenditure for the matched filter. Any more inaccurate, initial determination of the finger position performed at greater time intervals lead to a considerable reduction in the expenditure for the matched filter. To counteract the resultant degradation, the so-called early/late tracking is used. The early finger is positioned one half chip before, and the late finger one half chip after, the RAKE finger to be positioned (main finger). The energies of the early and late finger are calculated in the last stage of the RAKE receiver and only require little complexity. If the energies of the two fingers ≈ 0 - i.e. they have approximately the same low energy - the enclosed finger, The main finger, has an almost optimum position. If the energies of the tracking fingers are not approximately equal or ≠ 0, a repositioning takes place at W/n intervals, "W" being the chip frequency and "n" being the oversampling rate.

5. Soft handover

In the soft handover, the RAKE receiver receives signals sent with different scrambling/spreading codes from a number of base transceiver stations. The maximum number of RAKE fingers must be shared among the base transceiver stations in accordance with the quality of reception. This requires switching of the code generators which is dependent on the RAKE fingers. The multiplexer performing the switching operates with a maximum of L * W MHz, taking into consideration the early/late fingers.

During the soft handover, the base transceiver stations involved send the same user data to the mobile station. To control the transmitting power of the mobile station, the latter additionally receives an information item, the so-called TPC (transfer power control, compare FIGURES 1 and 2) bit which says whether the transmitting power has to be increased or decreased. For this reason, the different base transceiver station-dependent TPC bits must be decoded. The concluding or last part of the processing pipeline accumulates for this purpose symbols representing TPC bits, separated in accordance with received base transceiver station.

6. Flexiblity of the architecture with respect to word widths, clock rates and parallel processing

Depending on the field of application and the quality required (e.g. bit error rate) of the communication link (data, voice, etc.), a different number of RAKE fingers and word widths are required in the signal processing path. The proposed architecture allows simple adaptation. Greater word widths require lower clock rates of the individual processing units, the technology remaining the same. The processing power of the RAKE pipeline architecture can be increased by inserting parallel processing branches without greatly

increasing circuit complexity This provides for higher clock rates.

In the implementation of a RAKE receiver in hardware and/or software, however, savings can be achieved with respect to the number of function blocks used or their complexity by suitable mapping in software and hardware, and a greater flexibility in the parametrization - e.g. number of RAKE fingers.

In addition, the availability of fast technologies in the field of chip design (e.g. ASIC, FPGA) allows essential parts of the hardware to be used in time-division multiplex and thus to reduce the necessary number of logic gates.

An exemplary embodiment of the invention is explained with reference to FIGURE 6.

FIGURE 6 shows the pipeline architecture of a RAKE receiver in a block diagram.

FIGURE 6 shows RAKE receivers having a pipeline architecture, consisting of three pipeline stages, a first pipeline stage PLS1, a second pipeline stage PLS2 and a third pipeline stage PLS3 for L=8 fingers, soft handover with two base transceiver stations and early/late tracking. The pipeline structure shown is designed for one finger, but all fingers can be successively corrected. The clock rates specified relate to the RAKE receiver thus specified and are, therefore, a multiple of the chip frequency of 4 096 MChip. The word widths specified within the signal processing chain are derived from the boundary conditions for UMTS standardization (compare *SMG2 UMTSPhysical Layer Expert Group: "UTRA Physical Layer Description FDD Parts" vers. 0.4, 1998-06-25)*.

In principle, the architecture described can be extended to a different chip frequency "W", to any number of fingers "L", to "b" possible base transceiver stations in the case of a soft handover and

2*L fingers for the early/late tracking. Similarly, the architecture is flexible with respect to the choice of word widths used in the signal processing path.

The received signal r(t) is written in a dual-port RAM (DP-RAM) DPR with a frequency of 4 096 * n MHz (where n is the oversampling rate). The addresses for storing input data (chips) in the dual-port RAM DPR are generated by a first address counter AZ1.

To read the received chips out of the dual-port RAM DPR, an address (8 * 4 096 MHz clock) is calculated from the addition of a free-running second address counter AZ2 and the offsets dependent on the RAKE finger. The offsets are located in offset registers. For the early/late finger tracking to be implemented, two of the offset registers can be used for positioning the early and late finger. To recover the symbols, the data read out are multiplied in a first multiplier MUL1 by a spreading code generated by at least one code generator - two code generators CG1, CG2 in FIGURE 6 - and/or a scrambling code required for descrambling. In the case of simple codes, this is a sign operation whereas an additional addition is added in the case of complex codes.

In the soft handover case, the RAKE receiver receives signals sent with different scrambling/spreading codes from e.g. two base transceiver stations, base transceiver station 1 and base transceiver station 2. The maximum possible number of RAKE fingers must be shared among the base transceiver stations in accordance with the quality of reception. The scrambling/spreading codes are selected in a code combining/soft handover circuit CCSHS. This is why the code generators CG1, CG2 need to be switched in dependence on the RAKE fingers. A multiplexer

MUX which performs the switching operates with a maximum of 8 * 4 096 MHz in this example. In addition, the corresponding path delays are taken into consideration in this circuit CCSHS to be able to obtain a superimposition of the symbols of the individual signal paths in the correct phase (code combining).

The channel necessary for transmission distorts the signal. In the second pipeline stage PLS2, the channel estimator calculates the conjugate complex channel coefficients (weights) necessary for correcting the distortion from the received pilot sequence. The receiver, therefore, multiplies the recovered symbols of the individual RAKE fingers by their weights C_i* in a second multiplier MUL2. These weights are stored in a ring memory.

To avoid frequent estimation of the channel because this is a computationally intensive process, and to achieve a smaller time deviation of the coefficients from the ideal value, the weights are interpolated between two estimations in interpolation means IPM. This results in continual adding together of delta values.

In the last pipeline stage, the third pipeline stage PLS3, the chip energies of the individual fingers and thus the levels belonging to a symbol are accumulated in an accumulator AK successively over the period of one symbol. Symbols which represent TPC (transfer power control) bits must be accumulated separated by received base transceiver station. After each symbol, the accumulator AK must be reset.

For the early/late tracking, two separate accumulator registers AKR must be additionally provided for each early/late finger.

For each timeslot, overflow detector ÜD registers a bit overflow produced and deletes it at the beginning of the new timeslot.

If an overflow occurs, an AGC control ACGR must be informed that the input gain must be decreased.

The estimated value of the symbol $\underline{U}_m$ is present at the output of the RAKE receiver.

The following expression represents the general calculation of the estimated value $\underline{U}_m$ of a received symbol:

$$\underline{\quad}_m = \int_0^T \underline{r}(t) * \sum_{n=1}^{L} \underline{c}_n(t) * \underline{q}(t - n / W) dt$$

where r(t) is the received signal, $\underline{c}_n(t)$ is the weight and q(t) is the spreading/scrambling code. "L" describes the number of RAKE fingers and "1/W" is the duration of one chip.

In the pipeline structure with the three pipeline stages PLS1 ... PLS3 shown, two registers RG1, RG2 are connected between the pipeline stages for data buffering because no direct pipelining is possible because of different processing speeds in the pipeline stages.

Patent claims

1. A rake receiver for telecommunication systems with wireless telecommunication between mobile and/or stationary transceivers, especially in third-generation mobile radio systems, having the following feature:
a pipeline architecture comprising a number of pipeline stages (PLS1 ... PLS3) in which the individual signal processing steps or computing steps are processed as on a pipeline.

2. The rake receiver as claimed in claim 1, characterized in that there are three pipeline stages (PLS1 ... PLS3).

3. The rake receiver as claimed in claim 1 or 2, characterized in that there are registers (RG1, RG2) for data buffering between the pipeline stages.

4. The rake receiver as claimed in one of claims 1 to 3, characterized in that there are hardware circuits (DPR, AK, AKR) which can be used in time-division multiplex, in a first pipeline stage (PLS1).

5. The rake receiver as claimed in one of claims 1 to 4, characterized in that there is a first hardware circuit (CCSHS) which supports the soft handover in a first pipeline stage (PLS1).

6. The rake receiver as claimed in one of claims 1 to 5, characterized in that there is a second hardware circuit (CCSHS) which provides for code combining in a first pipeline stage (PLS1).

7. The rake receiver as claimed in one of claims 1 to 6, characterized in that there are interpolation means (IPM) which enable conjugate complex coefficients to be determined by means of interpolation between two channel estimations in a second pipeline stage (PLS2).

8. The rake receiver as claimed in one of claims 1 to 7, characterized in that the pipeline architecture can be flexibly adapted to word widths and clock rates by inserting parallel processing branches.

9. The rake receiver as claimed in one of claims 1 to 8, characterized in that there is a third hardware circuit (AK, AKR) which provides for low-expenditure early/late tracking of the rake fingers in a third pipeline stage (PLS3).

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FIG. 1

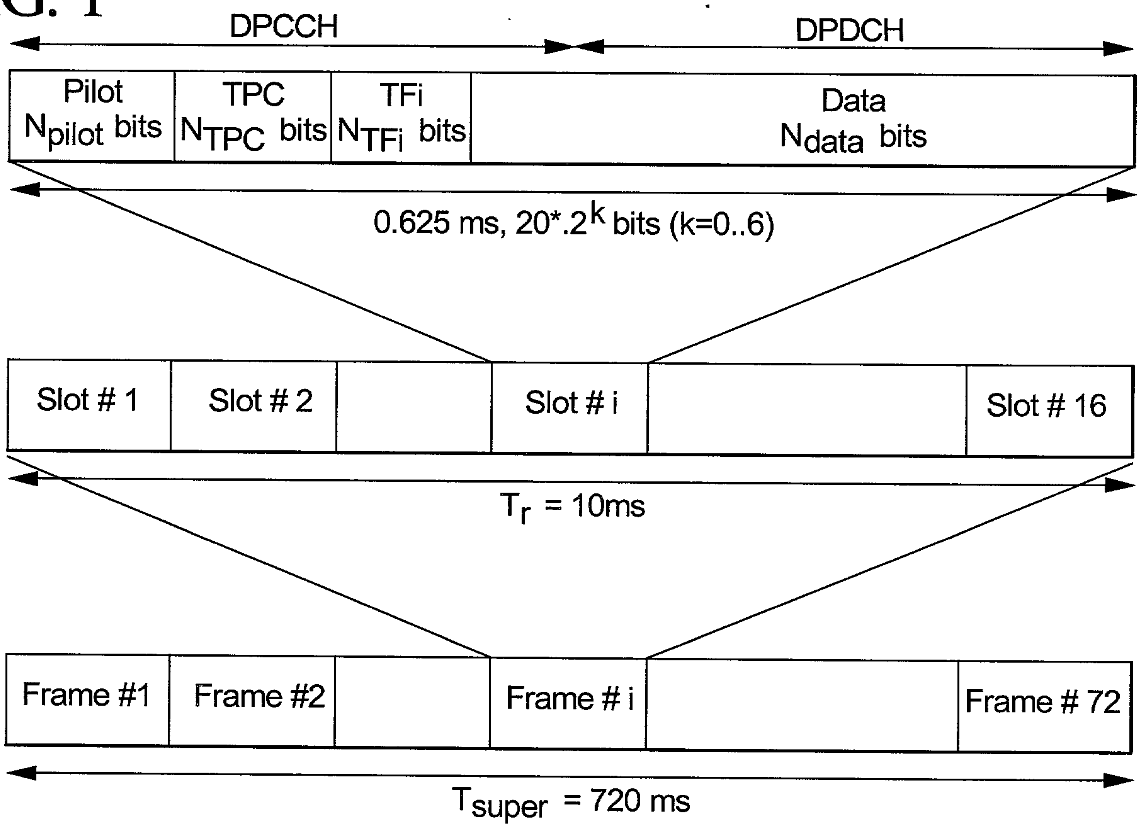

FIG. 2

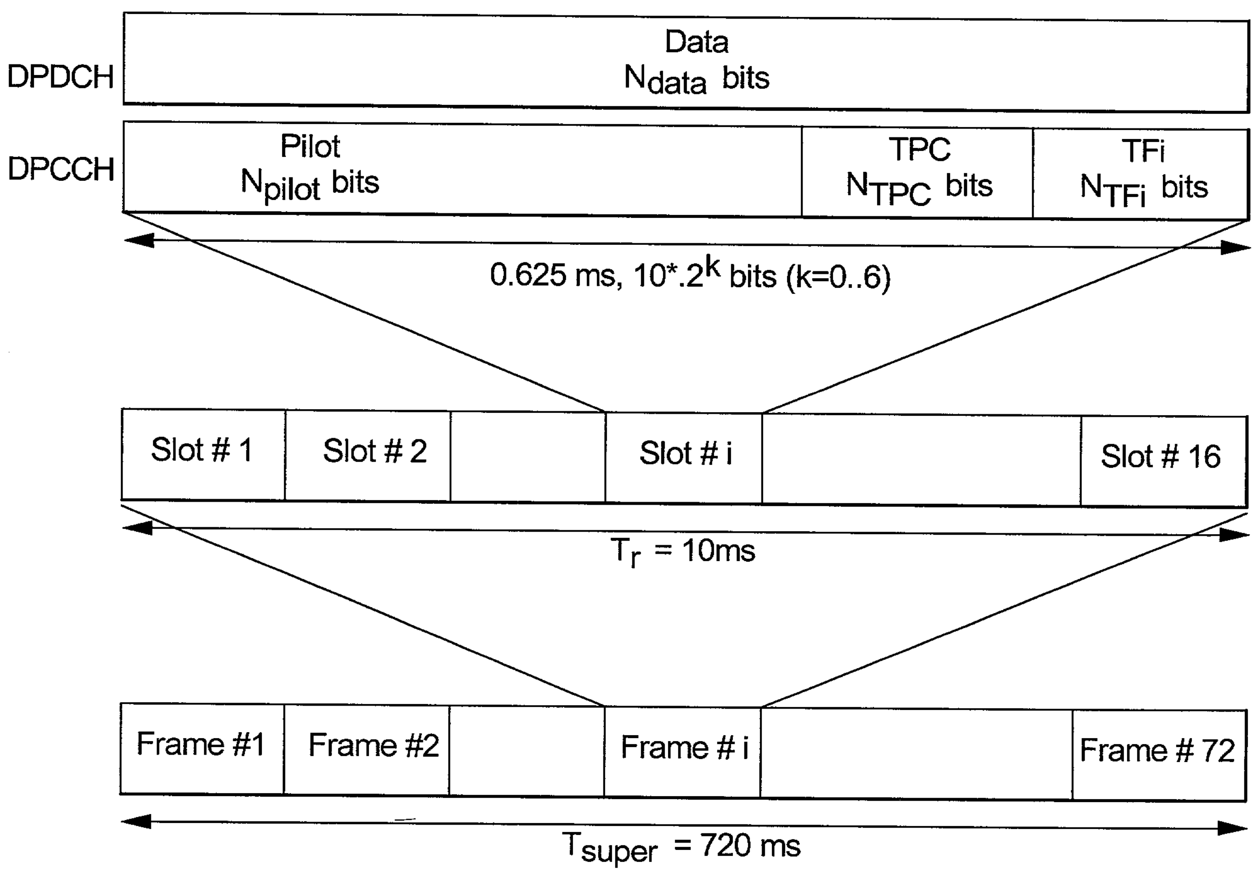

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FIG. 3

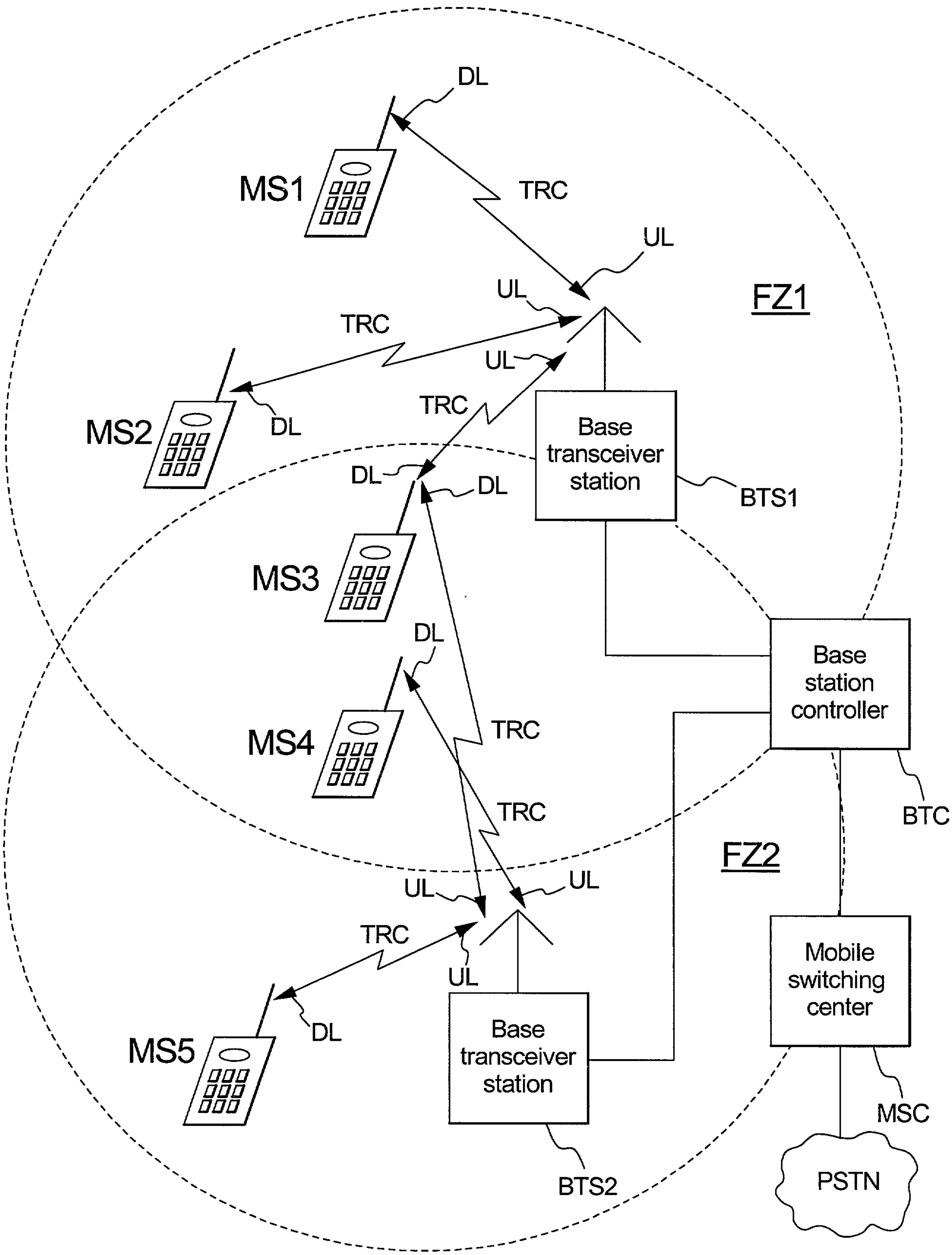

FIG. 4

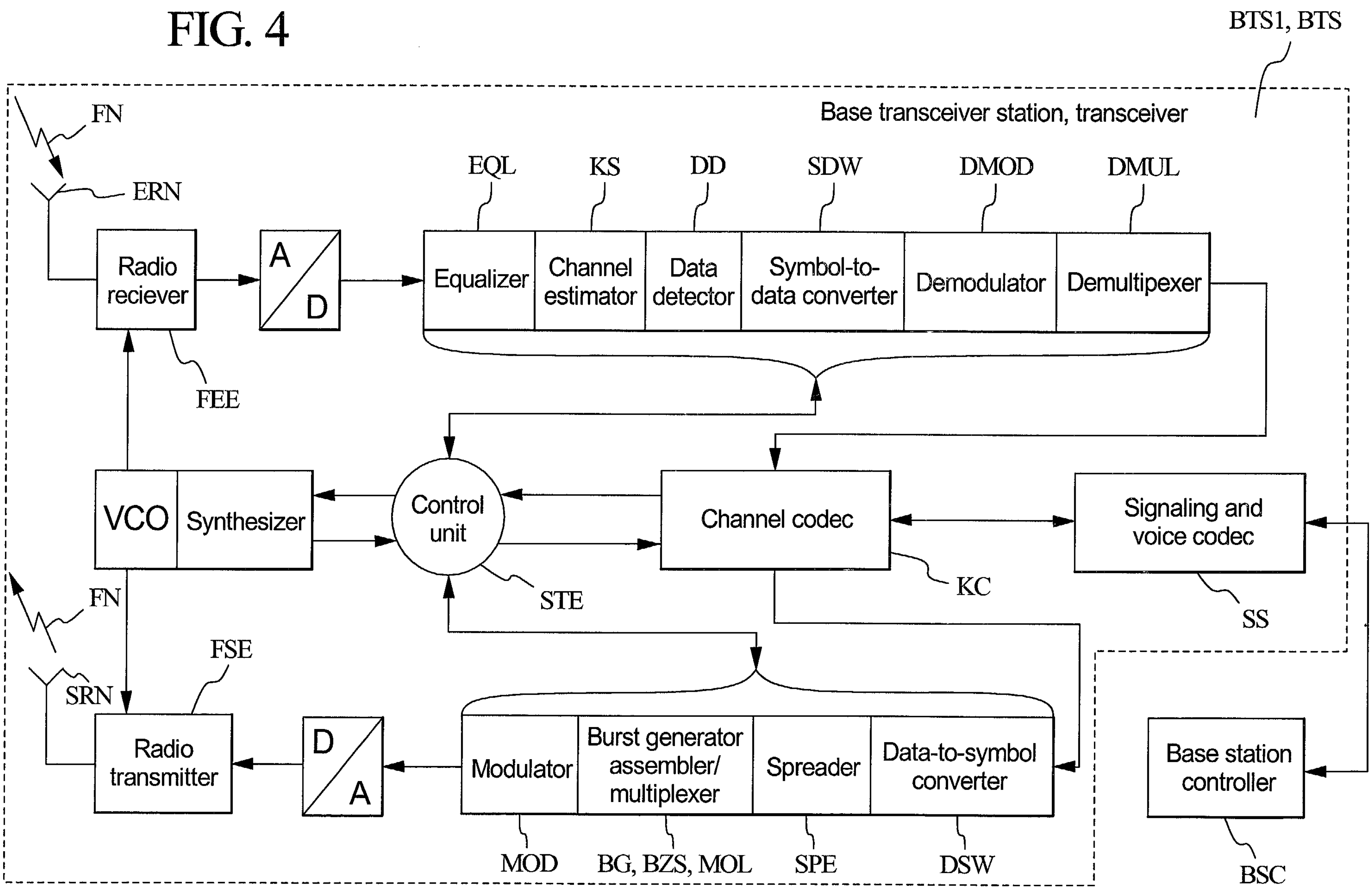

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FIG. 5

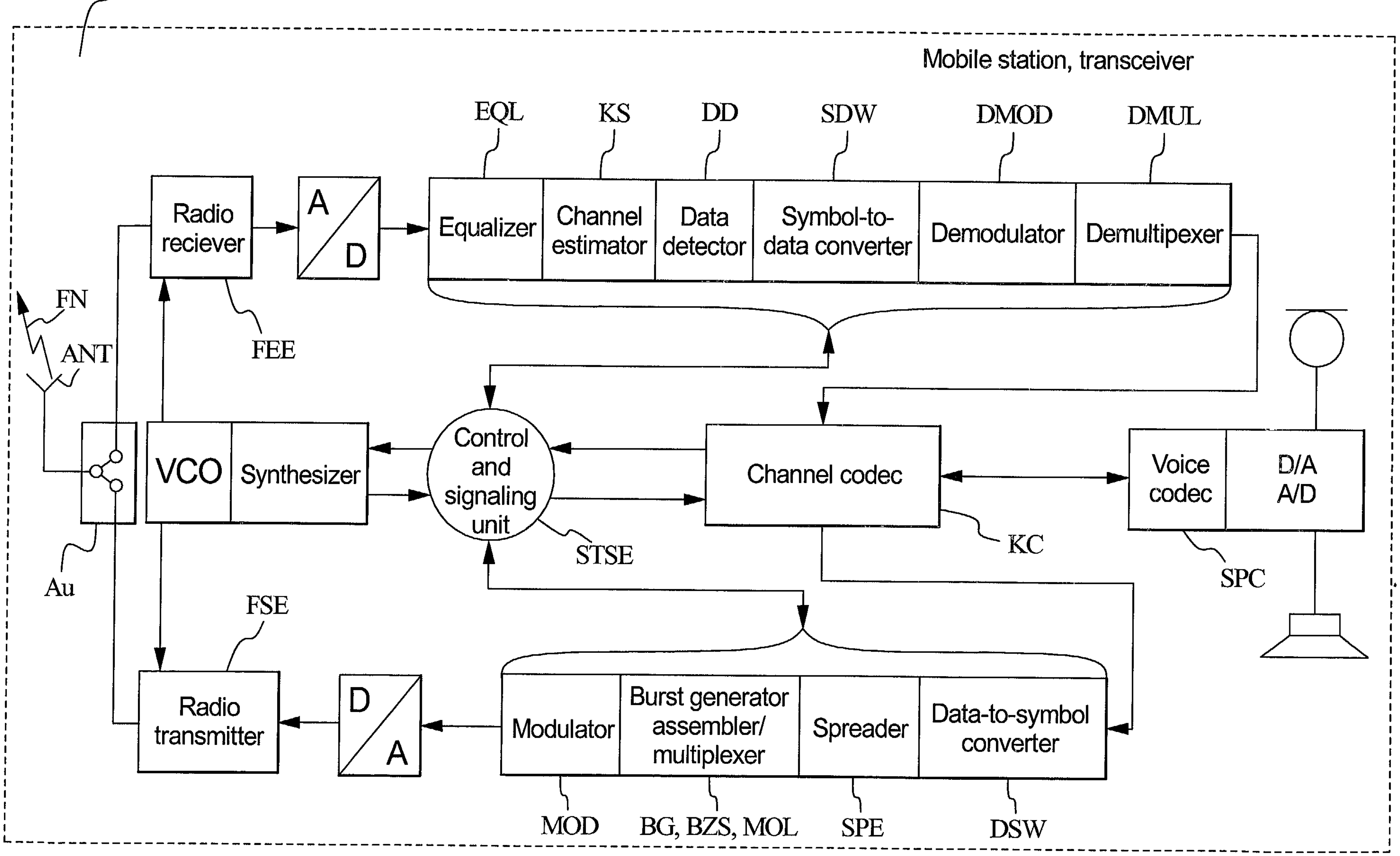

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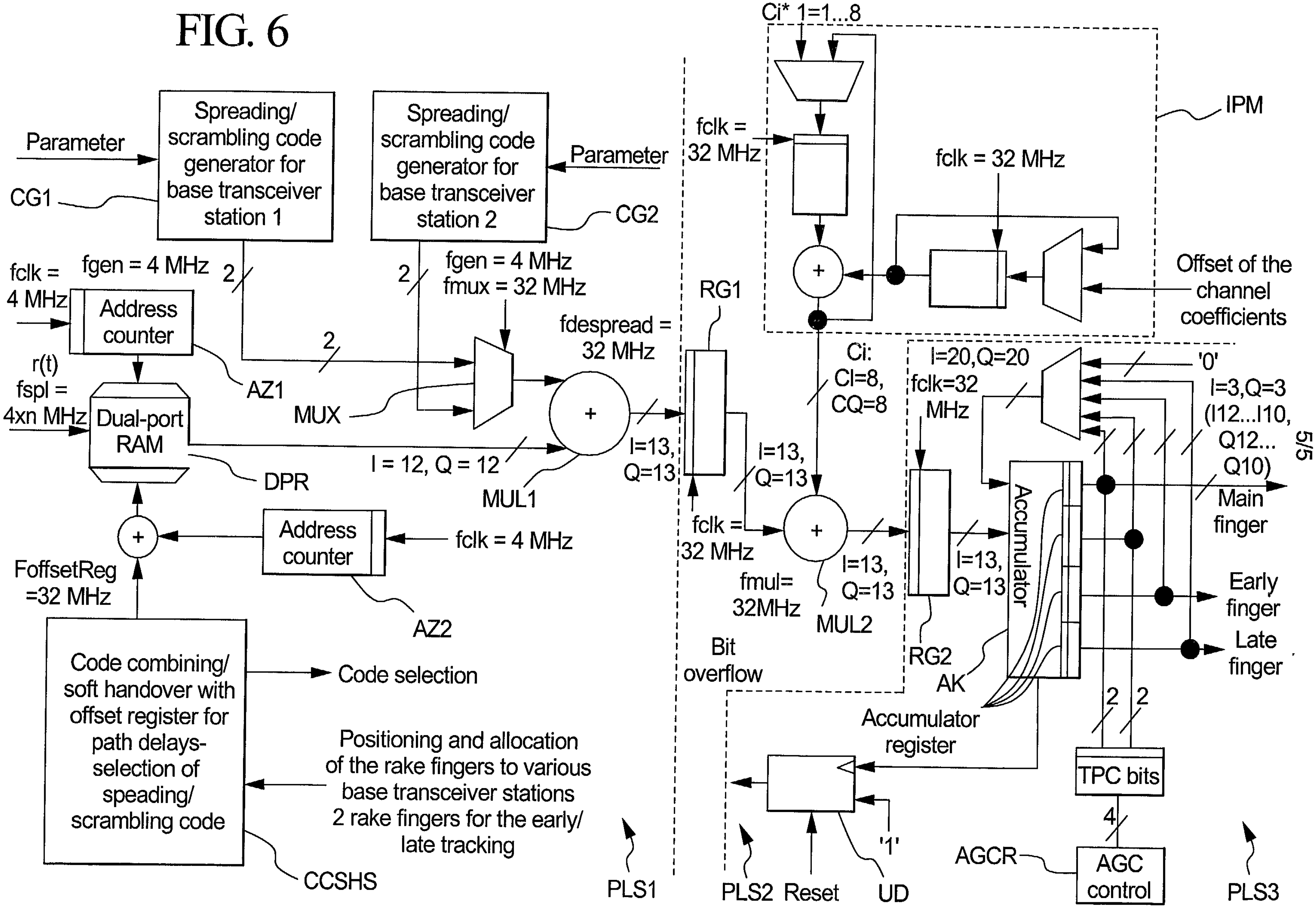
FIG. 6



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Declaration and Power of Attorney For Patent Application
Erklärung Für Patentanmeldungen Mit Vollmacht
German Language Declaration

Als nachstehend benannter Erfinder erkläre ich hiermit an Eides Statt:

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Rake-Empfaenger in Mobilfunksystemen der dritten Generation

deren Beschreibung

(zutreffendes ankreuzen)
☐ hier beigefügt ist.
☒ am 27.10.1999 als
PCT internationale Anmeldung
PCT Anmeldungsnummer PCT/DE99/03365
eingereicht wurde und am ______
abgeändert wurde (falls tatsächlich abgeändert).

Ich bestätige hiermit, dass ich den Inhalt der obigen Patentanmeldung einschliesslich der Ansprüche durchgesehen und verstanden habe, die eventuell durch einen Zusatzantrag wie oben erwähnt abgeändert wurde.

Ich erkenne meine Pflicht zur Offenbarung irgendwelcher Informationen, die für die Prüfung der vorliegenden Anmeldung in Einklang mit Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) von Wichtigkeit sind, an.

Ich beanspruche hiermit ausländische Prioritätsvorteile gemäss Abschnitt 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 119 aller unten angegebenen Auslandsanmeldungen für ein Patent oder eine Erfindersurkunde, und habe auch alle Auslandsanmeldungen für ein Patent oder eine Erfindersurkunde nachstehend gekennzeichnet, die ein Anmeldedatum haben, das vor dem Anmeldedatum der Anmeldung liegt, für die Priorität beansprucht wird.

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled

Rake receiver in third generation mobile radiotelephone systems

the specification of which

(check one)
☐ is attached hereto.
☒ was filed on 27.10.1999 as
PCT international application
PCT Application No. PCT/DE99/03365
and was amended on ______
(if applicable)

I hereby state that I have reviewed and understand the contents of the above identified specification, including the claims as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, §1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, §119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

German Language Declaration

Prior foreign appplications
Prioritát beansprucht

			Priority Claimed	
19849556.0	DE	27.10.1998	☒	☐
(Number)	(Country)	(Day Month Year Filed)	Yes	No
(Nummer)	(Land)	(Tag Monat Jahr eingereicht)	Ja	Nein
			☐	☐
(Number)	(Country)	(Day Month Year Filed)	Yes	No
(Nummer)	(Land)	(Tag Monat Jahr eingereicht)	Ja	Nein
			☐	☐
(Number)	(Country)	(Day Month Year Filed)	Yes	No
(Nummer)	(Land)	(Tag Monat Jahr eingereicht)	Ja	Nein

Ich beanspruche hiermit gemäss Absatz 35 der Zivilprozessordnung der Vereinigten Staaten, Paragraph 120, den Vorzug aller unten aufgeführten Anmeldungen und falls der Gegenstand aus jedem Anspruch dieser Anmeldung nicht in einer früheren amerikanischen Patentanmeldung laut dem ersten Paragraphen des Absatzes 35 der Zivilprozeßordnung der Vereinigten Staaten, Paragraph 122 offenbart ist, erkenne ich gemäss Absatz 37, Bundesgesetzbuch, Paragraph 1.56(a) meine Pflicht zur Offenbarung von Informationen an, die zwischen dem Anmeldedatum der früheren Anmeldung und dem nationalen oder PCT internationalen Anmeldedatum dieser Anmeldung bekannt geworden sind.

I hereby claim the benefit under Title 35. United States Code. §120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, §122, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, §1.56(a) which occured between the filing date of the prior application and the national or PCT international filing date of this application.

PCT/DE99/03365	27.10.1999		
(Application Serial No.) (Anmeldeseriennummer)	(Filing Date D, M, Y) (Anmeldedatum T, M, J)	(Status) (patentiert, anhàngig, aufgegeben)	(Status) (patented, pending, abandoned)
(Application Serial No.) (Anmeldeseriennummer)	(Filing Date D,M,Y) (Anmeldedatum T, M; J)	(Status) (patentiert, anhangig, aufgeben)	(Status) (patented, pending, abandoned)

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German Language Declaration

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POWER OF ATTORNEY: As a named inventor, I hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark Office connected therewith. *(list name and registration number)*

29177
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(Supply similar information and signature for third and subsequent joint inventors).

Geändert am 3.3.01 Andreas Falkenberg

09806083 . 032301

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Unterschrift des Erfinders Datum X Christoph Rohe 18.4.01	Inventor's signature Date
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Staatsangehörigkeit DE	Citizenship DE
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~~44793 BOCHUM~~ 44627 Herne	~~44793 BOCHUM~~ 44627 Herne
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Staatsangehörigkeit	Citizenship
Postanschrift	Post Office Address
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Unterschrift des Erfinders Datum	Inventor's signature Date
Wohnsitz ,	Residence ,
Staatsangehörigkeit	Citizenship
Postanschrift	Post Office Address
Voller Name des sexten Miterfinders:	Full name of sixth joint inventor:
Unterschrift des Erfinders Datum	Inventor's signature Date
Wohnsitz ,	Residence ,
Staatsangehörigkeit	Citizenship
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(Supply similar information and signature for third and subsequent joint inventors).